

Omnisemantics: Smooth Handling of Nondeterminism

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This paper gives an in-depth presentation of the omni-big-step and omni-small-step styles of semantic judgments. These styles describe operational semantics by relating starting states to sets of outcomes rather than to individual outcomes. A single derivation of these semantics for a particular starting state and program describes all possible nondeterministic executions (hence the name *omni*), whereas in traditional small-step and big-step semantics, each derivation only talks about one single execution. This restructuring allows for straightforward modeling of both nondeterminism and undefined behavior as commonly encountered in sequential functional and imperative programs. Specifically, omnisemantics inherently assert *safety*, i.e. they guarantee that none of the execution branches gets stuck, while traditional semantics need either a separate judgment or additional error markers to specify safety in the presence of nondeterminism.

Omnisemantics can be understood as an inductively defined weakest-precondition semantics (or more generally, predicate-transformer semantics) that does not involve invariants for loops and recursion but instead uses unrolling rules like in traditional small-step and big-step semantics. Omnisemantics were previously described in association with several projects, but we believe the technique has been underappreciated and deserves a well-motivated, extensive, and pedagogical presentation of its benefits. We also explore several novel aspects associated with these semantics, in particular their use in type-safety proofs for lambda calculi, partial-correctness reasoning, and forward proofs of compiler correctness for terminating but potentially nondeterministic programs being compiled to nondeterministic target languages. All results in this paper are formalized in Coq.

1 INTRODUCTION

Today, a typical project in rigorous reasoning about programming languages begins with an operational semantics (or maybe several), with proofs of key lemmas proceeding by induction on derivations of the semantics judgment. An extensive toolbox has been built up for formulating these relations, with common wisdom on the style to choose for each situation. With decades having passed since operational semantics became the standard technique in the 1980s, one might expect that the base of wisdom is sufficient. Yet, a style that we call *omnisemantics* has emerged in recent years as a new, powerful technique with numerous applications.

In short, omnisemantics relate starting states to their sets of possible outcomes, rather than to individual outcomes. The omni-big-step judgment takes the form $t/s \Downarrow Q$ and asserts that every possible evaluation starting from the configuration t/s reaches a final configuration that belongs to the set Q . This set Q is isomorphic to a postcondition from a Hoare triple. The omni-small-step judgment takes the form $t/s \longrightarrow P$. It asserts both that the configuration t/s can take one reduction step and that, for any step it might take, the resulting configuration belongs to the set P . On top of this judgment, one may define the *eventually* judgment $t/s \longrightarrow^\diamond P$, which asserts that every possible evaluation of t/s is safe and eventually reaches a configuration in the set P .

On the one hand, omnisemantics can be viewed as *operational semantics*, because they are not far from traditional operational semantics or executable interpreters. On the other hand, omnisemantics

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can be viewed as *axiomatic semantics*, because they are not far from reasoning rules; in particular, they directly give a practical, usable definition of a weakest-precondition judgment, which can be used for verifying concrete programs. The fact that they are both closely related to operational semantics and to axiomatic semantics is precisely the strength of omnisemantics.

To the best of our knowledge, the ideas of omnisemantics have been studied prior to the writing of this paper by three different groups of researchers. First, Schäfer et al. [2016] present an omni-big-step judgment for a nondeterministic source language of guarded commands, as well as for a deterministic target language with named continuations, using the term *axiomatic semantics* to refer to this style of semantics. They establish the correctness of a function that compiles terminating programs from the source language into the target language. Their proof is by induction on the derivation of an omni-big-step judgment for the *source* program rather than on a derivation for the *target* program, a key insight that we will discuss in Sections 1.3 and 6. They also present characterizations of program equivalence and present a proof of equivalence with traditional small-step semantics, though only in the case of a deterministic semantics. Second, Erbsen et al. [2021] make use of both omni-big-step semantics, applied to a high-level, core imperative language with external calls; and of omni-small-step semantics, applied to a low-level, RISC-V machine language. They call this style of semantics *CPS semantics*. They establish end-to-end compiler-correctness results for terminating programs. They also set up Separation Logic reasoning rules in weakest-precondition style. Third, Charguéraud [2020]’s course notes make use of omni-big-step semantics for the purpose of deriving Separation Logic triples, for both partial and total correctness. The language considered is a nondeterministic, imperative λ -calculus, with a substitution-based semantics. In particular, that work establishes the relationship between omni-big-step semantics and traditional big-step semantics, in the presence of nondeterminism.

Throughout the three pieces of work, the fundamental feature of omnisemantics being exploited is the ability to carry out proofs by induction on derivations that follow the flow of program execution, *with smooth handling of nondeterminism*. Indeed, nondeterministic choices result in universally quantified induction hypotheses at steps where nondeterministic choices are made. Before further presenting omnisemantics, we believe that it is useful to begin by presenting in more detail the several important problems that omnisemantics solve.

1.1 Feature #1: Stuck Terms and Nondeterminism

In an impure language, an execution may get stuck, for instance due to a division by zero or an out-of-bounds array access. In a nondeterministic language, some executions may get stuck while others do not. Thus, for an impure, nondeterministic language, the existence of a traditional big-step derivation for a starting configuration is not a proof that getting stuck is impossible.

How to fix the problem? A popular but cumbersome approach is to add errors as explicit outcomes (written *err* in the rules below), so that we can state theorems ruling out stuck terms. For example, if the semantics of an impure functional language includes the rule BIG-LET, it needs to be augmented with two additional rules for propagating errors: BIG-LET-ERR-1 and BIG-LET-ERR-2.

$$\begin{array}{c}
 \frac{t_1/s \Downarrow v_1/s' \quad ([v_1/x] t_2)/s' \Downarrow v/s''}{(\text{let } x = t_1 \text{ in } t_2)/s \Downarrow v/s''} \text{BIG-LET} \\
 \\
 \frac{t_1/s \Downarrow \text{err}}{(\text{let } x = t_1 \text{ in } t_2)/s \Downarrow \text{err}} \text{BIG-LET-ERR-1} \quad \frac{t_1/s \Downarrow v_1/s' \quad ([v_1/x] t_2)/s' \Downarrow \text{err}}{(\text{let } x = t_1 \text{ in } t_2)/s \Downarrow \text{err}} \text{BIG-LET-ERR-2}
 \end{array}$$

99 The set of inference rules grows significantly, and the very type signature of the relation is
100 complicated. Omni-big-step semantics provide a way to reason, in big-step style, about the absence
101 of stuck terms in nondeterministic languages without introducing error-propagation rules.
102

103 1.2 Feature #2: Termination and Nondeterminism

104 In a nondeterministic language, a total-correctness Hoare triple, written $\text{total}\{H\} t \{Q\}$, asserts that
105 in any state satisfying the precondition H , any execution of the term t terminates and reaches a
106 final state satisfying the postcondition Q . In foundational approaches, Hoare triples must be defined
107 in terms of or otherwise formally related to the operational semantics of languages.

108 When the (nondeterministic) semantics is expressed using the standard small-step relation, there
109 are two classical approaches to defining total-correctness Hoare triples. The first one involves
110 bounding the length of the execution. This approach not only involves tedious manipulation of
111 integer bounds, but it is also restricted to finitely branching forms of nondeterminism. The second
112 approach is to define total correctness as the conjunction of a partial-correctness property (if t
113 terminates, then it satisfies the postcondition) and of a separate, inductively defined termination
114 judgment. With both of these approaches, deriving reasoning rules for total-correctness Hoare
115 triples becomes much more tedious than in the case of partial correctness.

116 One may hope for simpler proofs using a big-step judgment. Indeed, Hoare triples inherently have
117 a big-step flavor. Moreover, for deterministic, sequential languages, the most direct way to derive
118 reasoning rules for Hoare triples is from the big-step evaluation rules. Yet, when the semantics of a
119 nondeterministic language is expressed using a traditional big-step judgment, we do not know of
120 any direct way to capture the fact that *all* executions terminate. Omni-big-step semantics provide a
121 direct definition of total-correctness Hoare triples with respect to a big-step-style, nondeterministic
122 semantics, in a way that leads to simple proofs of the Hoare-logic rules.
123

124 1.3 Feature #3: Simulation Arguments with Nondeterminism and Undefined Behavior

125 Many compiler transformations map source programs to target programs that require more steps
126 to accomplish the same work, because they must make do with lower-level primitives. Intuitively,
127 we like to think of a compiler transformation being correct in terms of *forward simulation*: the
128 transformation maps each step from the source program to a number of steps in the target program.
129 Yet, in the context of a nondeterministic language, such a result is famously insufficient even in the
130 special case of safely terminating programs. Concretely, compiler correctness requires showing *all*
131 possible behaviors of the target program correspond to possible behaviors of the source program.
132 A tempting approach is to establish a *backward simulation*, by showing that any step in the target
133 program can be matched by some number of steps in the source program. The trouble is that all
134 intermediate target-level states during a single source-level step need to be related to a source-level
135 state, severely complicating the simulation relation.
136

137 To avoid that hassle, most compilation phases from CompCert [Leroy 2009] are carried out on
138 *deterministic* intermediate languages, for which forward simulation implies backward simulation.
139 Yet, many realistic languages (C included) are not naturally seen as deterministic. CompCert
140 involves special effort to maintain determinism, through its celebrated memory model. Rather than
141 revealing pointers as integers, CompCert semantics allocate pointers deterministically, taking care
142 to trigger undefined behavior for any coding pattern that would be sensitive to the literal values
143 of pointers. As a result, any compiler transformations that modify allocation order require the
144 complex machinery of memory injections, to connect executions that use different deterministic
145 pointer values. Omnisemantics make it possible to retain the simplicity of forward simulation,
146 while keeping nondeterminism explicit.
147

1.4 Feature #4: Linear-Size Type-Safety Proofs

Type safety asserts that if a closed term is well-typed, then none of its possible evaluations gets stuck. A type-safety proof in the syntactic style [Wright and Felleisen 1994] reduces to a pair of lemmas: preservation and progress.

$$\begin{aligned} \text{PRESERVATION: } & E \vdash t : T \quad \wedge \quad t \longrightarrow t' \quad \Rightarrow \quad E \vdash t' : T \\ \text{PROGRESS: } & \emptyset \vdash t : T \quad \Rightarrow \quad (\text{isvalue } t) \vee (\exists t'. t \longrightarrow t') \end{aligned}$$

The Wright and Felleisen approach, although widely used, suffers from two limitations that can be problematic at the scale of real-world languages with hundreds of syntactic constructs.

The first limitation is that this approach requires performing two inductions over the typing judgment. Nontrivial language constructs are associated with nontrivial statements of their induction hypotheses, for which the same manual work needs to be performed twice, once in the preservation proof and once in the progress proof. Factoring out the cases makes a huge difference in terms of proof effort and maintainability.

The second limitation is associated with the case inspection involved in the preservation proof. Concretely, for each possible rule that derives the typing judgment ($E \vdash t : T$), one needs to select the applicable rules that can derive the reduction rule ($t \longrightarrow t'$) for that same term t . Typically, only a few reduction rules are applicable. The trouble is that fully rigorous checking of the proof must still inspect all of those cases to confirm their irrelevance. A direct Coq proof, of the form “induction H1; inversion H2”, results in a proof term of size quadratic in the size of the language¹. As we expect to handle each possible transition at most once, a proof that takes only linear work would be more satisfying. It would also avoid potential blow-up in the proof-checking time, for languages involving hundreds of constructs.

Interestingly, in the particular case of a deterministic language, there exists a strategy [Rompf and Amin 2016] for deriving type safety through a *single* inductive proof, which moreover avoids the quadratic case inspection. The key idea is to carry out an induction over the following statement: a well-typed term is either a value or can step to a term that admits the same type.

$$\emptyset \vdash t : T \quad \Rightarrow \quad (\text{isvalue } t) \vee (\exists t'. (t \longrightarrow t') \wedge (\emptyset \vdash t' : T))$$

Omnisemantics allow to generalize this approach to the case of nondeterministic languages. As we show in one of this paper’s original contributions, practical proofs of type safety can be carried out with respect to both omni-small-step and omni-big-step semantics.

1.5 Contributions and Contents of the Paper

The contributions of this paper are as follows.

- We present big-step and small-step omnisemantics for a standard imperative λ -calculus as well as for a standard imperative while language, which we believe should make the presentation more accessible than in prior publications. Moreover, we accompany this presentation with a Coq formalization of all definitions and proofs.²
- We explain four key beneficial features of omnisemantics: They provide a convenient way to reason about the absence of stuck terms (feature #1) and the absence of diverging terms (feature #2) in nondeterministic languages, they enable forward-simulation-based correctness proofs for compilers with nondeterministic target languages (feature #3), and they enable

¹Lean matches Coq, and a proof based on Agda’s flexible dependent pattern matching still takes superlinear time to check.

²The present paper would, in particular, provide a formal publication of the results covered by the chapter on nondeterminism and the chapter on partial correctness from Charguéraud’s *Separation Logic Foundations* course, Volume 6 of the Software Foundations series. These results originally covered only omni-big-step semantics but have been extended in 2021 to cover omni-small-step semantics as well.

type-safety proofs that avoid quadratic case inspection even in the case of a nondeterministic language (feature #4).

- We introduce the coinductive variant of omni-big-step semantics, which yields a partial-correctness judgment. This possibility was left as future work by Schäfer et al. [2016].
- We present numerous properties of omnisemantics, as well as their relationship to traditional operational semantics. Some of these properties were described in Erbsen et al. [2021] but only briefly. For example, the connection between traditional and omnisemantics only covered traditional small-step semantics with no undefined behavior, and small-step omnisemantics themselves were given one paragraph of description.
- We present in detail the proof techniques from two case studies on compiler-correctness results, adapted from Erbsen et al. [2021]’s prior work.
- We present a new case study illustrating an example of a correctness proof for a compiler transformation that *increases* the amount of nondeterminism. In contrast, work by Schäfer et al. [2016] and Erbsen et al. [2021] only considered transformations that *decrease* the amount of nondeterminism.

The paper is organized as follows.

- In Section 2, we introduce the omni-big-step judgment, which can be defined either inductively, to capture termination of all executions; or coinductively, in *partial-correctness* fashion. We also state and prove properties about the judgment, including the notion of smallest and largest admissible sets of outcomes.
- In Section 3, we introduce the omni-small-step judgment, as well as the *eventually* judgment defined on top of it and three practical reasoning rules associated with these judgments.
- In Section 4, we present type-safety proofs carried out with respect to either omni-small-step or omni-big-step semantics. We explain the improvement over the prior state of the art, as suggested in the earlier discussion of features #1 and #4.
- In Section 5, we explain how the omni-big-step judgment or the omni-small-step eventually judgment can be used to define Hoare triples and weakest-precondition predicates. We consider both partial and total correctness, and we show how the associated reasoning rules can be established via one-line proofs (recall feature #2). Moreover, we explain how one may derive the frame rule from Separation Logic.
- In Section 6, we demonstrate how omnisemantics can be used to prove that a compiler correctly compiles terminating programs, via forward-simulation proofs (recall feature #3). We illustrate this possibility through two case studies carried out on a while-language. The first one, “heapification” of pairs, increases the amount of nondeterminism; it involves omni-big-step semantics for both the source and the target language. The second one, introduction of stack allocation, decreases the amount of nondeterminism; it involves an omni-*big*-step semantics for the source language and an omni-*small*-step semantics for the target language.

Note that we leave it to future work to investigate how omnisemantics may be exploited to establish *full compiler correctness*, that is, not just the correctness of compilation for terminating programs but also that of programs that may crash, diverge, or perform infinitely many I/O interactions.

2 OMNI-BIG-STEP SEMANTICS

In the section, we introduce the omni-big-step judgment, written $t/s \Downarrow Q$. We use this judgment in particular for establishing type safety (§4.3), for setting up program logics (§5), and for establishing compiler-verification results (§6). To present the definition of this judgment, we consider an imperative, nondeterministic lambda-calculus, for which we first present the semantics in standard

big-step style (§2.1). We then discuss the properties and interpretation of the omni-big-step judgment (§2.2). In particular, we focus on why the set Q that appears in $t/s \Downarrow Q$ is interpreted as an *overapproximation* of the set of possible results, rather than as the *exact* set of possible results. We conclude this section by presenting the corresponding *coinductive* judgment, written $t/s \Downarrow^{\text{co}} Q$, which captures partial correctness in the sense that it allows for diverging executions (§2.4).

2.1 Definition of the Omni-Big-Step Judgment

Syntax. As a running example, we consider an imperative lambda-calculus, including a random-number generator `rand`. Both this operator and allocation are nondeterministic.

The grammar of the language appears next. The metavariable π ranges over primitive operations, v ranges over values, t ranges over terms, and x and f range over program variables. A value can be the unit value $\#$, a Boolean b , a natural number n , a pointer p , a primitive operator, or a closure³.

$$\begin{aligned} \pi &:= \text{add} \mid \text{rand} \mid \text{ref} \mid \text{free} \mid \text{get} \mid \text{set} \\ v &:= \# \mid b \mid n \mid p \mid \pi \mid \mu f. \lambda x. t \\ t &:= v \mid x \mid (t t) \mid \text{let } x = t \text{ in } t \mid \text{if } t \text{ then } t \text{ else } t \end{aligned}$$

For simplicity, we present evaluation rules by focusing first on programs in A-normal form: the `let`-binding construct is the only one that involves evaluation under a context. In an application $(t_1 t_2)$, the two terms must be either variables or values. Similarly, the condition of an `if`-statement must be either a variable or a value, and likewise for arguments of primitive operations. In §5.6, we present the *bind* rule, which enables the evaluation of subterms under all valid evaluation contexts.

Evaluation judgments. The standard big-step-semantics judgment for this language appears in Figure 1. States s are finite partial maps from pointers p to values v . The evaluation judgment $t/s \Downarrow v/s'$ asserts that the configuration t/s , made of a term t and an initial state s , may evaluate to the final configuration v/s' , made of a value v and a final state s' .

The corresponding omni-big-step semantics appears in Figure 2. Its evaluation judgment, written $t/s \Downarrow Q$, asserts that all possible evaluations starting from the configuration t/s reach final configurations that belong to the set Q . Observe how the standard big-step judgment $t/s \Downarrow v/s'$ describes the behavior of one possible execution of t/s , whereas the omni-big-step judgment describes the behavior of all possible executions of t/s . The set Q that appears in $t/s \Downarrow Q$ corresponds to an overapproximation of the set of final configurations: it may contain configurations that are not actually reachable by executing t/s . We return to that aspect in §2.3.

The set Q contains pairs made of values and states. Such a set can be described equivalently by a predicate of type “ $\text{val} \rightarrow \text{state} \rightarrow \text{Prop}$ ” or by a predicate of type “ $(\text{val} \times \text{state}) \rightarrow \text{Prop}$ ”. In this paper, in order to present definitions in the most idiomatic style, we use set-theoretic notation such as $(v, s) \in Q$ for stating semantics and typing rules, and we use the logic-oriented notation $Q v s$ when discussing program logics. (The type of Q may be generalized for languages that include exceptions; see Appendix C.)

Description of the evaluation rules. The base case is the rule OMNI-BIG-VAL: a final configuration v/s satisfies the postcondition Q if this configuration belongs to the set Q .

The `let`-binding rule OMNI-BIG-LET ensures that all possible evaluations of an expression `let $x = t_1$ in t_2` in state s terminate and satisfy the postcondition Q . First of all, we need all possible evaluations of t_1 to terminate. Let Q_1 denote (an overapproximation of) the set of results that t_1 may

³ In our Coq formalization, the grammar of values is restricted to *closed* values (i.e., values without free variables). This design choice significantly simplifies the reasoning about substitutions. One minor consequence is that the function construct needs to appear twice: once in the grammar of closed values and once in the grammar of terms.

<p>295</p> <p>296 BIG-VAL</p> $\frac{v/s \Downarrow v/s}{v/s \Downarrow v/s}$	<p>297 BIG-APP</p> $\frac{v_1 = (\mu f. \lambda x. t) \quad ([v_2/x] [v_1/f] t)/s \Downarrow v'/s'}{(v_1 v_2)/s \Downarrow v'/s'}$	<p>298 BIG-IF-TRUE</p> $\frac{t_1/s \Downarrow v'/s'}{(\text{if true then } t_1 \text{ else } t_2)/s \Downarrow v'/s'}$
<p>299 BIG-IF-FALSE</p> $\frac{t_2/s \Downarrow v'/s'}{(\text{if false then } t_1 \text{ else } t_2)/s \Downarrow v'/s'}$	<p>300 BIG-LET</p> $\frac{t_1/s \Downarrow v_1/s' \quad ([v_1/x] t_2)/s' \Downarrow v/s''}{(\text{let } x = t_1 \text{ in } t_2)/s \Downarrow v/s''}$	
<p>303 BIG-ADD</p> $\frac{}{(\text{add } n_1 n_2)/s \Downarrow (n_1 + n_2)/s}$	<p>304 BIG-RAND</p> $\frac{0 \leq m < n}{(\text{rand } n)/s \Downarrow m/s}$	<p>305 BIG-REF</p> $\frac{p \notin \text{dom } s}{(\text{ref } v)/s \Downarrow p/(s[p := v])}$
<p>306 BIG-FREE</p> $\frac{p \in \text{dom } s}{(\text{free } p)/s \Downarrow \#/(s \setminus p)}$	<p>307 BIG-GET</p> $\frac{p \in \text{dom } s}{(\text{get } p)/s \Downarrow (s[p])/s}$	<p>308 BIG-SET</p> $\frac{p \in \text{dom } s}{(\text{set } p v)/s \Downarrow \#/(s[p := v])}$

Fig. 1. Standard big-step semantics (for terms in A-normal form)

<p>313 OMNI-BIG-VAL</p> $\frac{(v, s) \in Q}{v/s \Downarrow Q}$	<p>314 OMNI-BIG-IF-TRUE</p> $\frac{t_1/s \Downarrow Q}{(\text{if true then } t_1 \text{ else } t_2)/s \Downarrow Q}$	<p>315 OMNI-BIG-IF-FALSE</p> $\frac{t_2/s \Downarrow Q}{(\text{if false then } t_1 \text{ else } t_2)/s \Downarrow Q}$
<p>316 OMNI-BIG-APP</p> $\frac{v_1 = \mu f. \lambda x. t_1 \quad ([v_1/f] [v_2/x] t_1)/s \Downarrow Q}{(v_1 v_2)/s \Downarrow Q}$	<p>317 OMNI-BIG-LET</p> $\frac{t_1/s \Downarrow Q_1 \quad (\forall (v', s') \in Q_1. ([v'/x] t_2)/s' \Downarrow Q)}{(\text{let } x = t_1 \text{ in } t_2)/s \Downarrow Q}$	<p>318 OMNI-BIG-ADD</p> $\frac{(n_1 + n_2, s) \in Q}{(\text{add } n_1 n_2)/s \Downarrow Q}$
<p>319 OMNI-BIG-RAND</p> $\frac{n > 0 \quad (\forall m. 0 \leq m < n \Rightarrow (m, s) \in Q)}{(\text{rand } n)/s \Downarrow Q}$	<p>320 OMNI-BIG-REF</p> $\frac{\forall p \notin \text{dom } s. (p, s[p := v]) \in Q}{(\text{ref } v)/s \Downarrow Q}$	
<p>321 OMNI-BIG-FREE</p> $\frac{p \in \text{dom } s \quad (\#t, s \setminus p) \in Q}{(\text{free } p)/s \Downarrow Q}$	<p>322 OMNI-BIG-GET</p> $\frac{p \in \text{dom } s \quad (s[p], s) \in Q}{(\text{get } p)/s \Downarrow Q}$	<p>323 OMNI-BIG-SET</p> $\frac{p \in \text{dom } s \quad (\#t, s[p := v]) \in Q}{(\text{set } p v)/s \Downarrow Q}$

Fig. 2. Omni-big-step semantics (for terms in A-normal form)

reach, as captured by the first premise $t_1/s \Downarrow Q_1$. One can think of Q_1 as the type of t_1 , in a very precise type system where any set of values can be treated as a type. The second premise asserts that, for any configuration v'/s' in that set Q_1 , we need all possible evaluations of the term $[v'/x] t_2$ in state s' to satisfy the postcondition Q .

The evaluation rule **OMNI-BIG-ADD** for an addition operation is almost like that of a value: it asserts that the evaluation of $\text{add } n_1 n_2$ in state s satisfies the postcondition Q if the pair $((n_1 + n_2), s)$ belongs to the set Q . The nondeterministic rule **OMNI-BIG-RAND** is more interesting. The term $\text{rand } n$ evaluates safely only if $n > 0$. Under this assumption, its result, named m in the rule, may be any integer in the range $[0, n)$. Thus, to guarantee that every possible evaluation of $\text{rand } n$ in a state s produces a result satisfying the postcondition Q , it must be the case that every pair of the form (m, s) with $m \in [0, n)$ belongs to the set Q .

The evaluation rule `OMNI-BIG-REF`, which describes allocation at a nondeterministically chosen, fresh memory address, follows a similar pattern. For every possible new address p , the pair made of p and the extended state $s[p := v]$ needs to belong to the set Q . The remaining rules, `OMNI-BIG-FREE`, `OMNI-BIG-GET` and `OMNI-BIG-SET`, are deterministic and follow the same pattern as `OMNI-BIG-ADD`, only with a side condition $p \in \text{dom } s$ to ensure that the address being manipulated does belong to the domain of the current state.

2.2 Properties of the Omni-Big-Step Judgment

In this section, we discuss some key properties of the omni-big-step judgment $t/s \Downarrow Q$. Recall that the metavariable Q denotes an overapproximation of the set of possible final configurations.

Total correctness. The predicate $t/s \Downarrow Q$ captures total correctness in the sense that it captures the conjunction of termination (all executions terminate) and partial correctness (if an execution terminates, then its final state satisfies the postcondition Q). Formally, let $t/s \Downarrow v/s'$ denote the standard big-step evaluation judgment, and let `terminates`(t, s) be a predicate that captures the fact that all executions of t/s terminate (a formal definition is given in [Appendix D](#)). We prove:

`OMNI-BIG-STEP-IFF-TERMINATES-AND-CORRECT` :

$$t/s \Downarrow Q \iff \text{terminates}(t, s) \wedge (\forall v s'. (t/s \Downarrow v/s') \Rightarrow (v, s') \in Q).$$

In particular, if we instantiate the postcondition Q with the *always-true* predicate, we obtain the predicate $t/s \Downarrow \{(v, s') \mid \text{True}\}$, which captures only the termination property.

Consequence rule. The judgment $t/s \Downarrow Q$ still holds when the postcondition Q is replaced with a larger set. In other words, the postcondition can always be weakened, like in Hoare logic.

$$\text{OMNI-BIG-CONSEQUENCE} : \quad t/s \Downarrow Q \wedge Q \subseteq Q' \Rightarrow t/s \Downarrow Q'$$

Strongest postcondition. If the omni-big-step judgment holds for at least one set, then there exists a smallest possible set Q for which $t/s \Downarrow Q$ holds. This set corresponds to the strongest possible postcondition Q , in the terminology of Hoare logic. Formally, if $t/s \Downarrow Q$ holds for at least one Q , then $t/s \Downarrow (\text{strongest-post } t \ s)$ holds, where the strongest postcondition is equal to the intersection of all valid postconditions.

$$\text{strongest-post } t \ s = \bigcap_{Q \mid (t/s \Downarrow Q)} Q = \{(v, s') \mid \forall Q, (t/s \Downarrow Q) \Rightarrow (v, s') \in Q\}$$

No derivations for terms that may get stuck. The fact that `rand 0` is a stuck term is captured by the fact that $(\text{rand } 0)/s \Downarrow Q$ does not hold for any Q . More generally, if one or more nondeterministic executions of t may get stuck, then we have: $\forall Q. \neg (t/s \Downarrow Q)$.

Relationship to standard big-step semantics. The standard big-step judgment $t/s \Downarrow v/s'$ relates one input configuration t/s to one single result configuration v/s' . The omni-big-step judgment, which relates inputs to sets of results, thus appears as an immediate generalization of the standard big-step judgment. The following two results formalizes their relationship.

First, if $t/s \Downarrow Q$ holds, then any final configuration for which the standard big-step judgment holds necessarily belongs to the set Q .

$$\text{OMNI-BIG-AND-BIG-INV} : \quad t/s \Downarrow Q \wedge t/s \Downarrow v/s' \Rightarrow (v, s') \in Q$$

Second, if $t/s \Downarrow Q$ holds, then there exists at least one evaluation according to the standard big-step judgment whose final configuration belongs to the set Q .

$$\text{OMNI-BIG-TO-ONE-BIG} : \quad t/s \Downarrow Q \Rightarrow \exists v s'. t/s \Downarrow v/s' \wedge (v, s') \in Q$$

<p style="text-align: center;">PRECISE-BIG-VAL</p> $\frac{}{v/s \Downarrow' \{(v, s)\}}$	<p style="text-align: center;">PRECISE-BIG-REF</p> $\frac{}{(\text{ref } v)/s \Downarrow' \{(p, s[p := v]) \mid p \notin \text{dom } s\}}$
<p style="text-align: center;">PRECISE-BIG-RAND</p> $\frac{n > 0}{(\text{rand } n)/s \Downarrow' \{(m, s) \mid 0 \leq m < n\}}$	<p style="text-align: center;">PRECISE-BIG-LET</p> $\frac{t_1/s \Downarrow' Q_1 \quad \forall (v', s') \in Q_1. ([v'/x] t_2)/s' \Downarrow' Q'_{(v', s')}}{(\text{let } x = t_1 \text{ in } t_2)/s \Downarrow' \bigcup_{(v', s') \in Q_1} Q'_{(v', s')}}}$

Fig. 3. Selected rules defining a *precise* variant of omni-big-step semantics, written $t/s \Downarrow' Q$.

A corollary asserts that if $t/s \Downarrow' Q$ holds with Q being a singleton set made of a unique final configuration v/s' , then the standard big-step judgment holds for that configuration.

$$\text{OMNI-BIG-SINGLETON:} \quad t/s \Downarrow' \{(v, s')\} \Rightarrow t/s \Downarrow' v/s'$$

Particular case of deterministic languages. In a deterministic language, an input configuration t/s may evaluate to at most one configuration v/s' . In such a case, the strongest postcondition is reduced to the singleton set $\{(v, s')\}$.

Nonempty outcome sets. Observe that the judgment $t/s \Downarrow' Q$, as defined in Fig. 2, can only hold for a nonempty set Q . When designing omni-big-step rules for a new language, one has to be careful not to accidentally include rules that allow derivations of empty outcome sets for some programs. To illustrate the matter, consider the term “rand 0”. According to the standard big-step semantics, this term is stuck because the rule BIG-RAND requires a positive argument to rand. In the omni-big-step semantics, if we were to omit the premise $n > 0$ in the rule OMNI-BIG-RAND, we would be able to derive $(\text{rand } 0)/s \Downarrow' Q$ for any s and Q . Indeed, the premise $\forall m. 0 \leq m < n \Rightarrow (m, s) \in Q$ becomes vacuously true when n is nonpositive.

A similar subtlety appears in the rule OMNI-BIG-REF, where the fresh location p must be picked fresh from the domain of s . This quantification could become vacuously true if the semantics allowed for infinite states or if the set of memory locations were finite. (We discuss in §6.5 the treatment of a language whose semantics account for a finite memory.)

The likelihood of unadequate formalization due to missing premises might be viewed as the main weakness of omnisemantics. Yet, if needed, additional confidence can easily be restored at the cost of minor additional work: one may consider a standard small-step semantics as reference (i.e., as part of the trusted code base), then relate it to the corresponding omni-big-step semantics and use the latter to carry out big-step style, inductive proofs on nondeterministic executions.

2.3 About the Overapproximation of the Set of Results

The omni-big-step judgment $t/s \Downarrow' Q$ associates an initial configuration t/s with a postcondition Q , which denotes an *overapproximation* of the set of possible final configurations. One may thus wonder: why not associate it with a *precise* set of results? In this section, we show that it is technically possible to define a *precise* judgment, but at the same time we argue why that judgment is much less practical to work with than the *overapproximating* omni-big-step judgment.

The precise judgment, written $t/s \Downarrow' Q$, is precise in the sense that it relates a configuration t/s to *at most one* set of results Q . This precise judgment, like the overapproximating omni-big-step judgment, guarantees safety: a judgment $t/s \Downarrow' Q$ can be derived for some Q if and only if none of the possible executions of t/s can get stuck. Thus, the precise judgment relates a *safe* configuration t/s to exactly one Q .

442 Figure 3 shows selected rules from the definition of the precise judgment, written $t/s \Downarrow Q$. The
 443 rule PRECISE-BIG-VAL relates a value v in a state s to the singleton set made of the pair (v, s) . The
 444 rule PRECISE-BIG-REF relates the term $(\text{ref } v)$ in a state s to the set of pairs made of a location p
 445 fresh from s and of the state s updated at location p with the value v . Observe how this compares
 446 with the rule OMNI-BIG-REF, which only requires that set of pairs to be included in the result set Q .
 447 The rule PRECISE-BIG-RAND follows a similar pattern, only with the premise $n > 0$ to ensure that
 448 the term is not stuck.

449 Most interesting is the rule PRECISE-BIG-LET. Its first premise involves an intermediate set Q_1 ,
 450 which denotes *exactly* the set of results that t_1 can produce when executed in the input state s . The
 451 second premise describes, for each result (v', s') from the set Q_1 , the evaluation of $([v'/x] t_2)$ in
 452 state s' . The result of the execution is asserted to be exactly a set of configurations written $Q'_{(v', s')}$.
 453 Here Q' denotes a (possibly infinite) family of postconditions, indexed by the possible results of t_1 .
 454 The final postcondition of the term $(\text{let } x = t_1 \text{ in } t_2)$ is obtained by taking the union over that family
 455 of postconditions.⁴

456 In practice, working with indexed families of postconditions introduces significant overhead,
 457 compared with the overapproximating omni-big-step judgment. Moreover, for practical applications
 458 such as type-checking or program verification (either using weakest preconditions or Hoare triples),
 459 we are only interested in overapproximations of the semantics. For such applications, building the
 460 overapproximation on top of a precise judgment would only introduce a level of indirection. For
 461 other situations where a notion of exact set of results might be desirable, typically for metatheoretical
 462 results (e.g., completeness results), we can always refer to the *strongest postcondition*, which, as
 463 explained earlier, can be formalized as the intersection of all valid postconditions.

464 In summary, we believe that it is interesting to know that a precise judgment can be defined, as
 465 it might be useful in other contexts, but for the applications that we have in mind the overapproxim-
 466 ating omni-big-step judgment appears much better suited.

468 2.4 Coinductive Interpretation of the Omni-Big-Step Judgment

470 Let $t/s \Downarrow^{\text{co}} Q$ denote the judgment defined by the coinductive interpretation of the same set of
 471 rules as for the inductively defined judgment $t/s \Downarrow Q$, i.e., rules from Fig. 2. The coinductive
 472 interpretation allows for infinite derivation trees, thus the coinductive omni-big-step judgment can
 473 be used to capture properties of nonterminating executions.

474 More precisely, the judgment $t/s \Downarrow^{\text{co}} Q$ asserts that every possible execution of configuration
 475 t/s either diverges or terminates in a final configuration satisfying Q . In particular, this judgment
 476 rules out the possibility for an execution of t/s to get stuck, and it can be used to express type
 477 safety, as detailed in §4. The judgment $t/s \Downarrow^{\text{co}} Q$ can also be used to define partial-correctness
 478 Hoare triples, as detailed in §5.

479 Formally, we can relate the meaning of $t/s \Downarrow^{\text{co}} Q$ to the small-step characterization of partial
 480 correctness as follows: for every execution prefix, the configuration reached is either a value
 481 satisfying the postcondition, or it is a term that can be reduced further. Below, $t/s \longrightarrow t'/s'$
 482 denotes the standard small-step evaluation judgment (defined in Appendix G), and val denotes the
 483

484
485
486

487 ⁴In Coq, we model sets with elements of type A as functions from A to propositions, thus Q_1 is represented as a function that
 488 takes a value and a state and returns a proposition, Q' is a function that takes a value, a state, another value, another state
 489 and returns a proposition, and the union over the family of results is written $\lambda v'' s''. \exists v' s'. Q_1 v' s' \wedge Q' v' s' v'' s''$.

490

<p>491 OMNI-SMALL-APP</p> $\frac{v_1 = (\mu f. \lambda x. t) \quad ([v_2/x] [v_1/f] t, s) \in P}{(v_1 v_2)/s \longrightarrow P}$	<p>492 OMNI-SMALL-IF-TRUE</p> $\frac{P(t_1, s)}{(\text{if true then } t_1 \text{ else } t_2)/s \longrightarrow P}$	<p>493 OMNI-SMALL-IF-FALSE</p> $\frac{P(t_2, s)}{(\text{if false then } t_1 \text{ else } t_2)/s \longrightarrow P}$
<p>494</p> <p>495 OMNI-SMALL-LET-CTX</p> $\frac{t_1/s \longrightarrow P_1 \quad (\forall (t'_1, s') \in P_1. ((\text{let } x = t'_1 \text{ in } t_2), s') \in P)}{(\text{let } x = t_1 \text{ in } t_2)/s \longrightarrow P}$	<p>496 OMNI-SMALL-LET</p> $\frac{([v_1/x] t_2, s) \in P}{(\text{let } x = v_1 \text{ in } t_2)/s \longrightarrow P}$	
<p>499</p> <p>500 OMNI-SMALL-ADD</p> $\frac{(n_1 + n_2, s) \in P}{(\text{add } n_1 \ n_2)/s \longrightarrow P}$	<p>501 OMNI-SMALL-RAND</p> $\frac{n > 0 \quad (\forall m \in [0, n]. (m, s) \in P)}{(\text{rand } n)/s \longrightarrow P}$	<p>502 OMNI-SMALL-REF</p> $\frac{(\forall p \notin \text{dom } s. (p, s[p := v]) \in P)}{(\text{ref } v)/s \longrightarrow P}$
<p>503</p> <p>504 OMNI-SMALL-FREE</p> $\frac{p \in \text{dom } s \quad (\#t, s \setminus p) \in P}{(\text{free } p)/s \longrightarrow P}$	<p>505 OMNI-SMALL-GET</p> $\frac{p \in \text{dom } s \quad (s[p], s) \in P}{(\text{get } p)/s \longrightarrow P}$	<p>506 OMNI-SMALL-SET</p> $\frac{p \in \text{dom } s \quad (\#t, s[p := v]) \in P}{(\text{set } p \ v)/s \longrightarrow P}$

Fig. 4. Omni-small-step semantics (for terms in A-normal form)

constructor that injects values into the grammar of terms.

CO-OMNI-BIG-IFF-SAFE-AND-CORRECT

$$t/s \Downarrow^{\text{CO}} Q \iff \forall s' t'. (t/s \longrightarrow^* t'/s') \Rightarrow \left(\exists v. t' = \text{val } v \wedge (v, s') \in Q \right) \vee \left(\exists t'' s''. t'/s' \longrightarrow t''/s'' \right)$$

The judgment $t/s \Downarrow^{\text{CO}} Q$ can also be used to characterize divergence, by instantiating Q as the empty set: the predicate $t/s \Downarrow^{\text{CO}} \emptyset$ asserts that every possible execution of t/s diverges. Because the judgment $t/s \Downarrow^{\text{CO}} Q$ is covariant in Q , the predicate $t/s \Downarrow^{\text{CO}} \emptyset$ holds if and only if the predicate $t/s \Downarrow^{\text{CO}} Q$ holds for any Q . In summary, we formally characterize divergence as follows.

$$\text{diverges } t \ s \equiv (t/s \Downarrow^{\text{CO}} \emptyset) \quad \text{diverges } t \ s \iff \forall Q. (t/s \Downarrow^{\text{CO}} Q)$$

3 OMNI-SMALL-STEP SEMANTICS

In this section, we present the omni-small-step judgment, written $t/s \longrightarrow P$. Here, P denotes a set of pairs each made of a term and a state. We then present the *eventually* judgment, written $t/s \longrightarrow^\diamond P$. We use these judgments in particular for establishing type-safety (§4.1) and compiler-verification (§6.6) results.

3.1 The Omni-Small-Step Judgment

The omni-small-step judgment, written $t/s \longrightarrow P$, asserts that the configuration t/s can take one reduction step and that, for any step it might take, the resulting configuration belongs to the set P . It is defined by the rules from Fig. 4. There is one per small-step transition. The interesting rules are those involving nondeterminism, namely OMNI-SMALL-RAND and OMNI-SMALL-REF, which follow a pattern similar to the corresponding omni-big-step rules. Observe also how the rule OMNI-SMALL-LET-CTX handles the case of a reduction that takes place in the evaluation context of a let-binding, by quantifying over an intermediate set of results named P_1 .

We prove that the judgment $t/s \longrightarrow P$ captures the expected property w.r.t. the standard small-step judgment: the configuration t/s can make a step, and for every step it might take, it reaches a

540 configuration in P .

$$541 \quad \text{OMNI-SMALL-STEP-IFF-PROGRESS-AND-CORRECT} \\ 542 \quad t/s \longrightarrow P \iff (\exists t's'. t/s \longrightarrow t'/s') \wedge (\forall t's'. t/s \longrightarrow t'/s' \Rightarrow (t', s') \in P) \\ 543$$

544 3.2 The “Eventually” Judgment

545 The judgment $t/s \longrightarrow^\diamond P$ captures the property that every possible evaluation of t/s is safe and
546 eventually reaches a configuration in the set P . Here, P denotes a set of configurations—it is not
547 limited to being a set of *final* configurations like in the previous section. The judgment $t/s \longrightarrow^\diamond P$
548 is defined inductively by the following two rules. The first one asserts that the judgment is satisfied if
549 t/s belongs to P . The second one asserts that the judgment is satisfied if t/s is not stuck and that for
550 any configuration t'/s' that it may reduce to, the predicate $t'/s' \longrightarrow^\diamond P$ holds. The latter property is
551 expressed using the omni-small-step judgment $t/s \longrightarrow P'$, where P' denotes an overapproximation
552 of the set of configurations t'/s' to which t/s may reduce.

$$553 \quad \begin{array}{c} \text{EVENTUALLY-HERE} \\ (t, s) \in P \\ \hline t/s \longrightarrow^\diamond P \end{array} \quad \begin{array}{c} \text{EVENTUALLY-STEP} \\ t/s \longrightarrow P' \quad (\forall (t', s') \in P'. t'/s' \longrightarrow^\diamond P) \\ \hline t/s \longrightarrow^\diamond P \end{array}$$

554 If Q denotes a set of *final* configurations, then the judgment $t/s \longrightarrow^\diamond Q$ can be viewed as a
555 particular case of the judgment $t/s \longrightarrow^\diamond P$, where P denotes a set of configurations. We prove that
556 $t/s \longrightarrow^\diamond Q$ matches our omni-big-step judgment $t/s \Downarrow Q$.

$$557 \quad \text{EVENTUALLY-IFF-OMNI-BIG-STEP:} \quad t/s \longrightarrow^\diamond Q \iff t/s \Downarrow Q$$

562 3.3 Chained Rule and Cut Rule for the “Eventually” Judgment

563 To apply the rule EVENTUALLY-STEP, one needs to provide upfront an intermediate postcondition P' .
564 Doing so is not always convenient. It turns out that we can leverage the omni-small-step judgment
565 $t/s \longrightarrow P'$ to provide an introduction rule for $t/s \longrightarrow^\diamond P$ that does not require providing P' upfront.
566 This rule, which we call the *chained* version of EVENTUALLY-STEP, admits the statement shown
567 below. It reads as follows: if every possible step of t/s reduces in one step to a configuration that
568 eventually reaches a configuration from the set P , then every possible evaluation of t/s eventually
569 reaches a configuration from the set P .

$$570 \quad \text{EVENTUALLY-STEP-CHAINED:} \quad t/s \longrightarrow \{(t', s') \mid t'/s' \longrightarrow^\diamond P\} \Rightarrow t/s \longrightarrow^\diamond P$$

571 One may wonder why we did not use this rule directly in the inductively defined judgment, and
572 the reason is Coq’s *strict positivity* requirement. The considerations for encoding sequencing here
573 are similar to those discussed in Appendix A in the context of the omni-big-step let-binding rule.

574 Another interesting property of the judgment $t/s \longrightarrow^\diamond P$ is its cut rule, which is derivable. It
575 asserts the following: if every possible evaluation of t/s eventually reaches a configuration in the
576 set P' , and if every configuration from the set P' eventually reaches a configuration from the set P ,
577 then every possible evaluation of t/s eventually reaches a configuration from the set P .

$$578 \quad \text{EVENTUALLY-CUT:} \quad t/s \longrightarrow^\diamond P' \wedge (\forall (t', s') \in P'. t'/s' \longrightarrow^\diamond P) \Rightarrow t/s \longrightarrow^\diamond P$$

579 This cut rule also admits a *chained* version, which reads as follows: if every possible evaluation of
580 t/s eventually reaches a configuration that itself eventually reaches a configuration from the set P ,
581 then every possible evaluation of t/s eventually reaches a configuration from the set P .

$$582 \quad \text{EVENTUALLY-CUT-CHAINED:} \quad t/s \longrightarrow^\diamond \{(t', s') \mid t'/s' \longrightarrow^\diamond P\} \Rightarrow t/s \longrightarrow^\diamond P$$

583 The cut rule and the chained rules are particularly handy to work with, as we illustrate in §6.6.

3.4 Coinductive Interpretation of the Omni-Small-Step Judgment

Let $t/s \longrightarrow_{\text{co}}^{\diamond} P$ denote the coinductive interpretation of the two rules that define $t/s \longrightarrow^{\diamond} P$. Divergence can be captured by instantiating P as the empty set. We prove that the judgment $t/s \longrightarrow_{\text{co}}^{\diamond} \emptyset$ is equivalent to the standard small-step characterization of divergence, which asserts that any execution prefix may be extended with at least one additional step.

CO-EVENTUALLY-EMPTY-IFF-SMALL-STEP-DIVERGES

$$t/s \longrightarrow_{\text{co}}^{\diamond} \emptyset \iff \forall s't'. (t/s \longrightarrow^* t'/s') \Rightarrow (\exists t''s''. t'/s' \longrightarrow t''/s'')$$

Besides, we can relate the coinductive omni-small-step judgment $t/s \longrightarrow_{\text{co}}^{\diamond} P$ to the coinductive omni-big-step judgment $t/s \Downarrow^{\text{co}} Q$ defined in §2.4. Here again, we let Q denote a set of *final* configurations. We prove the following equivalence.

$$\text{CO-EVENTUALLY-IFF-CO-OMNI-BIG-STEP: } t/s \longrightarrow_{\text{co}}^{\diamond} Q \iff t/s \Downarrow^{\text{co}} Q$$

The proofs of these two equivalences CO-EVENTUALLY-IFF-CO-OMNI-BIG-STEP, CO-EVENTUALLY-EMPTY-IFF-SMALL-STEP-DIVERGES, as well as the proof of CO-OMNI-BIG-IFF-SAFE-AND-CORRECT from §3.4, are interesting in that they involve yet another judgment. This judgment, written $t/s \longrightarrow_{\text{co}}^* Q$, is defined in terms of the standard small-step semantics, by taking the coinductive interpretation of the following two rules.

$$\frac{\text{EVENTUALLY}^{\text{'-HERE}} \quad (v, s) \in Q}{v/s \longrightarrow_{\text{co}}^* Q} \quad \frac{\text{EVENTUALLY}^{\text{'-STEP}} \quad (\exists t's'. t/s \longrightarrow t'/s') \quad (\forall t's'. (t/s \longrightarrow t'/s') \Rightarrow (t'/s' \longrightarrow_{\text{co}}^* Q))}{t/s \longrightarrow_{\text{co}}^* Q}$$

The desired equivalences are established in three steps. First, we prove that the standard small-step characterization of partial correctness that appears in the statement of CO-OMNI-BIG-IFF-SAFE-AND-CORRECT (§3.4) is equivalent to this new coinductive judgment $t/s \longrightarrow_{\text{co}}^* Q$. The proof is relatively straightforward because both of these characterizations are expressed using small-step transitions.

Second, we prove that the *co-eventually* judgment $t/s \longrightarrow_{\text{co}}^{\diamond} Q$ is equivalent to $t/s \longrightarrow_{\text{co}}^* Q$. The proof is relatively straightforward because the coinductive definitions for these two judgments share a similar structure. As a corollary, by instantiating Q as the empty set, we establish CO-EVENTUALLY-EMPTY-IFF-SMALL-STEP-DIVERGES.

Third, we prove that the *co-omni-big-step* judgment $t/s \Downarrow^{\text{co}} Q$ is equivalent to $t/s \longrightarrow_{\text{co}}^* Q$. This third proof is the most challenging, especially for establishing the implication from the small-step style judgment to the big-step style judgment. The proof involves a key intermediate lemma, which consists of an inversion rule for let-bindings: if $(\text{let } x = t_1 \text{ in } t_2)/s \longrightarrow_{\text{co}}^* Q$ holds, then there exists a set Q_1 such that $t_1/s \longrightarrow_{\text{co}}^* Q_1$ and $\forall (v_1, s') \in Q_1. ([v_1/x] t_2)/s' \longrightarrow_{\text{co}}^* Q$ hold. The proof of this key lemma itself relies on two auxiliary results, whose purpose is to justify that we can take as witness for Q_1 the strongest postcondition of t_1/s . The first one asserts that $(\text{let } x = t_1 \text{ in } t_2)/s \longrightarrow_{\text{co}}^* Q$ implies $t_1/s \longrightarrow_{\text{co}}^* \{(v_1, s') \mid t_1/s \longrightarrow^* v_1/s'\}$. The second one asserts that $(\text{let } x = t_1 \text{ in } t_2)/s \longrightarrow_{\text{co}}^* Q$ and $t_1/s \longrightarrow^* v_1/s'$ imply $([v_1/x] t_2)/s' \longrightarrow_{\text{co}}^* Q$. We refer to our Coq development for details.

A key observation about all the proofs involved in §2 and §3 is that they are constructive⁵. In particular, we are able to establish equivalences between *coinductive omni-big-step semantics* and small-step style semantics *without* recourse to classical logic. This contrast with *coinductive big-step semantics* [Leroy and Grall 2009], whose connection to small-step semantics requires classical logic. We discuss this aspect further in the related work section (§7).

⁵ The proofs that we present do not exploit classical logic axioms. However, we do not provide a *machine-checked proof* that our proofs are constructive. Indeed, our Coq development is building on top of general-purpose libraries that exploit classical logic in various places. It would require a prohibitive amount of work to reimplement these libraries constructively.

4 TYPE-SAFETY PROOFS USING OMNISEMANTICS

In this section, we show how the omni-small-step and omni-big-step judgments may be used to carry out type-safety proofs. We illustrate the proof structures using simple types (STLC). As a warm-up, we begin with a presentation of type safety on the restriction to the state-free fragment of our running-example language.

For this section, we need to consider a different semantics for the random-number generator. Indeed, the current rule OMNI-BIG-RAND asserts that the program is stuck if $\text{rand } n$ is invoked with an argument $n \leq 0$. Since here we are interested in proving that well-typed programs do not get stuck, let us consider a modified semantics, where $\text{rand } n$ is turned into a total function that returns 0 when $n \leq 0$.

$$\frac{\text{OMNI-BIG-RAND-COMPLETE} \quad \forall m. 0 \leq m < \max(n, 1) \Rightarrow (m, s) \in Q}{(\text{rand } n)/s \Downarrow Q} \quad \frac{\text{OMNI-SMALL-RAND-COMPLETE} \quad \forall m. 0 \leq m < \max(n, 1) \Rightarrow (m, s) \in P}{(\text{rand } n)/s \longrightarrow P}$$

Additionally, for this section, we also exclude the primitive operation `free`, which is not type-safe.

The grammar of types, written T , appears below.

$$T := \text{unit} \mid \text{bool} \mid \text{int} \mid T \rightarrow T \mid \text{ref } T$$

A typing environment, written E , maps variable names to types. The judgment $\vdash v : T$ asserts that the closed value v admits the type T . The judgment $E \vdash t : T$ asserts that the term t admits type T in the environment E . We let \mathbb{V} denote the set of terms that are either values or variables—recall that we consider A -normal forms to simplify the presentation. The typing rules are essentially standard, apart from the fact that they involve side conditions of the form $t \in \mathbb{V}$ to constrain terms to be in A -normal form. We include here two example rules; the other rules are given in appendix E.

$$\frac{\text{TYP-LET} \quad E \vdash t_1 : T_1 \quad E, x : T_1 \vdash t_2 : T_2}{E \vdash (\text{let } x = t_1 \text{ in } t_2) : T_2} \quad \frac{\text{TYP-RAND} \quad E \vdash t_1 : \text{int} \quad t_1 \in \mathbb{V}}{E \vdash (\text{rand } t_1) : \text{int}}$$

4.1 Omni-Small-Step Type-Safety Proof for a State-Free Language

A *stuck term* is a term that is not a value and that cannot take a step. Type safety asserts that if a closed term t is well-typed, then none of its possible evaluations gets stuck. In other words, if t reduces in a number of steps to t' , then t' is either a value or can further reduce.

TYPE-SAFETY (STATE-FREE LANGUAGE):

$$(\emptyset \vdash t : T) \wedge (t \longrightarrow^* t') \Rightarrow (\text{isvalue } t') \vee (\exists t''. t' \longrightarrow t'')$$

The traditional approach to establishing type safety is by proving the *preservation* and *progress* properties [Pierce 2002; Wright and Felleisen 1994].

PRESERVATION (STATE-FREE LANGUAGE):

$$E \vdash t : T \wedge t \longrightarrow t' \Rightarrow E \vdash t' : T$$

PROGRESS (STATE-FREE LANGUAGE):

$$\emptyset \vdash t : T \Rightarrow (\text{isvalue } t) \vee (\exists t'. t \longrightarrow t')$$

Each of these proofs is most typically carried out by induction on the typing judgment. One difficulty that might arise in the type-preservation proof for a large language with dozens (if not hundreds) of typing rules is the fact that one needs, for each case of the typing judgment $E \vdash t : T$, to inspect all the potential cases of the reduction judgment $t \longrightarrow t'$. This inspection is not really quadratic in practice, because one can filter out applicable rules based on the shape of the term t . Nevertheless, a typical Coq proof performing “intros HT HR; induction HT; inversion HR” does produce a proof term whose size is quadratic in the number of term constructs. Coq users have experienced performance challenges with quadratic-complexity proof terms when formalizing PL metatheory [Monin and Shi 2013].

687 Interestingly, in the particular case of a deterministic language, there exists a known strategy
 688 (e.g., of Rompf and Amin [2016]) to reformulate the preservation and progress statements in a way
 689 that not only factors out the two into a single statement but also can be proved with a linear-size
 690 proof term. This combined statement, shown below, asserts that a well-typed term t is either a
 691 value or can make a step towards a term t' that admits the same type.

692 INDUCTION-FOR-TYPE-SAFETY, STATE-FREE, STANDARD SMALL-STEP, DETERMINISTIC

$$693 \quad \emptyset \vdash t : T \quad \Rightarrow \quad (\text{isvalue } t) \vee (\exists t'. (t \longrightarrow t') \wedge (\emptyset \vdash t' : T))$$

694 As we explain next, this approach can be generalized to the case of nondeterministic languages
 695 using the omni-small-step judgment. Let us write $t \longrightarrow P$ for the judgment that corresponds to
 696 $t/s \longrightarrow P$ without the state argument. We can state type safety by considering for the postcondition
 697 P the set of terms t' that admit the same type as t .

700 LEMMA 4.1 (INDUCTION-FOR-TYPE-SAFETY, STATE-FREE, OMNI-SMALL-STEP, NON-DETERMINISTIC).

$$701 \quad \emptyset \vdash t : T \quad \Rightarrow \quad (\text{isvalue } t) \vee (t \longrightarrow \{t' \mid (\emptyset \vdash t' : T)\})$$

702 PROOF. The proof is carried out by induction on the typing judgment. For the case where t
 703 is a value, the left part of the disjunction applies. For all other cases, the right part needs to be
 704 established. We next detail two representative proof cases.

705 CASE 1: the term t has been typed using rule TYP-RAND. In this case, the term t has the form
 706 “rand t_1 ”. The rule concludes $\emptyset \vdash (\text{rand } t_1) : \text{int}$, from the premise $\emptyset \vdash t_1 : \text{int}$ and the premise
 707 $t_1 \in \mathbb{V}$. The latter means that t_1 is either a value or a variable (recall that we assume A-normal
 708 form to simplify the presentation). Because t_1 typechecks in the empty environment, it cannot be a
 709 variable. Thus, it must be a value, and because this value has type int, it must be an integer value. (In
 710 other words, $\emptyset \vdash t_1 : \text{int}$ must have been derived using the rules TYP-VAL and VTYP-INT stated in
 711 appendix E.) Let us call n this integer. We need to establish: $(\text{rand } n) \longrightarrow \{t' \mid (\emptyset \vdash t' : \text{int})\}$. Recall
 712 the rule OMNI-SMALL-RAND-COMPLETE introduced at the start of §4. We apply this rule (ignoring
 713 the state component), and need to establish its premise: $\forall m. 0 \leq m < \max(n, 1) \Rightarrow m \in \{t' \mid (\emptyset \vdash$
 714 $t' : \text{int})\}$. Consider an integer m such that $0 \leq m < \max(n, 1)$. We are left to prove $\emptyset \vdash m : \text{int}$,
 715 which is derivable from the rules TYP-VAL and VTYP-INT.

716 CASE 2: the term t has been typed using rule TYP-LET. In this case, the term t has the form
 717 “let $x = t_1$ in t_2 ”. The rule concludes $\emptyset \vdash (\text{let } x = t_1 \text{ in } t_2) : T$, from the two premises $\emptyset \vdash t_1 : T_1$
 718 and $x : T_1 \vdash t_2 : T$. We need to prove $(\text{let } x = t_1 \text{ in } t_2) \longrightarrow \{t' \mid (\emptyset \vdash t' : T)\}$. By the induction
 719 hypothesis applied to the first assumption, either t_1 is a value, or $t_1 \longrightarrow \{t'_1 \mid (\emptyset \vdash t'_1 : T_1)\}$.

720 In the first subcase, t_1 is a value; let us call it v_1 . We exploit OMNI-SMALL-LET, and are left to
 721 justify $([v_1/x] t_2) \in \{t' \mid (\emptyset \vdash t' : T)\}$, that is, $\emptyset \vdash ([v_1/x] t_2) : T$. This result follows from the
 722 standard substitution lemma applied to $x : T_1 \vdash t_2 : T$ and to $\emptyset \vdash v_1 : T_1$.

723 In the second subcase, we have $t_1 \longrightarrow \{t'_1 \mid (\emptyset \vdash t'_1 : T_1)\}$. To prove $(\text{let } x = t_1 \text{ in } t_2) \longrightarrow$
 724 $\{t' \mid (\emptyset \vdash t' : T)\}$, we exploit OMNI-SMALL-LET-CTX with $P_1 = \{t'_1 \mid (\emptyset \vdash t'_1 : T_1)\}$. We need
 725 to justify the second premise of that rule: $\forall t'_1 \in P_1. (\text{let } x = t'_1 \text{ in } t_2) \in \{t' \mid (\emptyset \vdash t' : T)\}$.
 726 Consider a particular t'_1 . The assumption $t'_1 \in P_1$ is equivalent to $\emptyset \vdash t'_1 : T_1$. The proof obligation
 727 $(\text{let } x = t'_1 \text{ in } t_2) \in \{t' \mid (\emptyset \vdash t' : T)\}$ is equivalent to $\emptyset \vdash (\text{let } x = t'_1 \text{ in } t_2) : T$. This result follows
 728 from the rule TYP-LET applied to the facts $\emptyset \vdash t'_1 : T_1$ and $x : T_1 \vdash t_2 : T$. \square

The statement `INDUCTION-FOR-TYPE-SAFETY` above entails the preservation property (for empty environments) and the progress property. We prove once-and-for-all that the statement of `INDUCTION-FOR-TYPE-SAFETY` entails the `TYPE-SAFETY` property.⁶

4.2 Omni-Small-Step Type-Safety Proof for an Imperative Language

Let us now generalize the results from the previous section to account for memory operations.

A store-typing environment, written S , is a map from locations to types. The typing judgment for values is extended with a store-typing environment, taking the form $S \vdash v : T$. Likewise, the typing judgment for terms is extended to the form $S; E \vdash t : T$. The store-typing entity S only plays a role in the typing rule for memory locations. The rules for typing memory locations and memory operations are standard; they appear in [Appendix F](#).

The type-safety property asserts that the execution of any well-typed term, starting from the empty state, does not get stuck. In the statement below, \emptyset denotes an empty state or an empty store typing, whereas \emptyset denotes, as before, the empty typing context.

`TYPE-SAFETY`:

$$(\emptyset; \emptyset \vdash t : T) \wedge (t/\emptyset \longrightarrow^* t'/s') \Rightarrow (\text{isvalue } t') \vee (\exists t''s''. t'/s' \longrightarrow t''/s'')$$

To establish a type-safety result by induction on a reduction sequence, one needs to introduce a typing judgment for stores. A store s admits type S , written $\vdash s : S$, if and only if s and S have the same domain and, for any location p in the domain, $s[p]$ admits the type $S[p]$. Formally:

$$\vdash s : S \quad \equiv \quad (\text{dom } s = \text{dom } S) \wedge (\forall p \in \text{dom } s. S; \emptyset \vdash s[p] : S[p])$$

The preservation and progress lemmas associated with the traditional approach to proving type safety are updated as shown below. In particular, the preservation lemma requires the output state to admit a type that extends the store typing associated with the input state ($S' \supseteq S$).

`PRESERVATION`: $t/s \longrightarrow t'/s' \wedge \vdash s : S \wedge S; \emptyset \vdash t : T$

$$\Rightarrow \exists S' \supseteq S. \vdash s' : S' \wedge S'; \emptyset \vdash t' : T$$

`PROGRESS`: $S; \emptyset \vdash t : T \wedge \vdash s : S \Rightarrow (\text{isvalue } t) \vee (\exists t's'. t/s \longrightarrow t'/s')$

In contrast, using the omni-small-step judgment, we can establish type safety through a single induction on the typing judgment. To that end, we formulate a lemma in terms of the predicate $t/s \longrightarrow P$, instantiating the set P as the set of configurations t'/s' such that t' admits the same type as t and such that s' admits a type that extends the type of s .

`INDUCTION-FOR-TYPE-SAFETY (OMNI-SMALL-STEP, WITH STATE)`

$$\begin{aligned} & (S; \emptyset \vdash t : T) \wedge (\vdash s : S) \\ \Rightarrow & (\text{isvalue } t) \vee (t/s \longrightarrow \{(t', s') \mid \exists S' \supseteq S. (\vdash s' : S') \wedge (S'; \emptyset \vdash t' : T)\}) \end{aligned}$$

4.3 Omni-Big-Step Type-Safety Proof for an Imperative Language

Traditionally, a big-step safety proof can only be carried out if the semantics is completed using error-propagation rules. Here, we demonstrate how to establish type safety with respect to an omni-big-step judgment, without any need for error-propagation rules. First, we introduce the construct $\llbracket T/S \rrbracket$ to denote the set of possible outputs produced by a term of type T , well-typed in a store of type S . Second, we describe the statement and proof for type safety.

⁶The generic entailment from `INDUCTION-FOR-TYPE-SAFETY` to `TYPE-SAFETY` holds for any typing judgment of the form $\emptyset \vdash t : T$ and for any judgment $t \longrightarrow P$ related to the small-step judgment $t \longrightarrow t'$ in the expected way, that is, satisfying the property `OMNI-SMALL-STEP-IFF-PROGRESS-AND-CORRECT` from [§3.2](#).

785 Consider a type T and a store typing S . We define $\llbracket T/S \rrbracket$ as the set of final configurations of the
 786 form v/s such that the state s admits a type S' that extends S , and the value v admits type T , under
 787 the store typing S' . The extension S' involved here accounts for the fact that the evaluation of a
 788 term t of type T may perform allocation operations that extend the store in which t is well-typed.

$$789 \llbracket T/S \rrbracket \equiv \{(v, s) \mid \exists S' \supseteq S. (\vdash s : S') \wedge (S' \vdash v : T)\}$$

791 A key lemma involved in the type soundness proof asserts that, if S' is a store typing that enforces
 792 more constraints than another store typing S , then $\llbracket T/S' \rrbracket$ is a smaller set than $\llbracket T/S \rrbracket$.

793 LEMMA 4.2 (CONFIGURATION-TYPING-SUBSET).

$$794 S' \supseteq S \Rightarrow \llbracket T/S' \rrbracket \subseteq \llbracket T/S \rrbracket$$

796 PROOF. Assume $S' \supseteq S$. Consider a pair $(v, s) \in \llbracket T/S' \rrbracket$. By definition, there exists S'' such that
 797 $S'' \supseteq S'$ and $\vdash s : S''$ and $S'' \vdash v : T$. By transitivity, $S'' \supseteq S$. We conclude that $(v, s) \in \llbracket T/S \rrbracket$
 798 holds, by taking S'' as witness for the existential quantifier in the definition of $\llbracket T/S \rrbracket$. \square

800 We are now ready to state type safety. The coinductive omni-big-step judgment $t/s \Downarrow^{\text{co}} \llbracket T/S \rrbracket$
 801 asserts that any evaluation of t/s executes safely, without ever getting stuck; and that if an evaluation
 802 reaches a final configuration v/s' , then this configuration satisfies the postcondition $\llbracket T/S \rrbracket$. Given
 803 our definition of $\llbracket T/S \rrbracket$, the judgment $t/\emptyset \Downarrow^{\text{co}} \llbracket T/\emptyset \rrbracket$ thus captures exactly the type-safety property
 804 associated with the typing judgment $\emptyset; \emptyset \vdash t : T$. Type safety may be established by proving the
 805 following statement by coinduction.

806 LEMMA 4.3 (COINDUCTION-FOR-TYPE-SAFETY, OMNI-BIG-STEP, NON-DETERMINISTIC).

$$807 S; \emptyset \vdash t : T \wedge \vdash s : S \Rightarrow t/s \Downarrow^{\text{co}} \llbracket T/S \rrbracket$$

809 PROOF. For technical reasons, the Coq coinduction tactic needs to be applied to the following
 810 statement, which introduces an intermediate set Q .

$$811 S; \emptyset \vdash t : T \wedge \vdash s : S \wedge \llbracket T/S \rrbracket \subseteq Q \Rightarrow t/s \Downarrow^{\text{co}} Q$$

813 Observe that this alternative statement is logically equivalent to the previous one: on the one hand,
 814 we can instantiate Q as $\llbracket T/S \rrbracket$; on the other hand, we can exploit OMNI-BIG-CONSEQUENCE to prove
 815 $t/s \Downarrow^{\text{co}} Q$ from $t/s \Downarrow^{\text{co}} \llbracket T/S \rrbracket$ and $\llbracket T/S \rrbracket \subseteq Q$.

816 We carry out a proof by coinduction on that alternative statement. The coinduction hypothesis
 817 asserts that we can *assume* the alternative statement to hold, provided that we have already applied
 818 at least one evaluation rule (i.e., a *coinductive constructor*) to the conclusion at hand ($t/s \Downarrow^{\text{co}} Q$).

819 The first step of the proof is to perform a case analysis on the typing hypothesis $S; \emptyset \vdash t : T$. We
 820 then consider each of the possible typing rules one-by-one. Let us consider two representative proof
 821 cases: the case of `rand` and the case of a `let`-binding. In each case, the assumptions are $S; \emptyset \vdash t : T$
 822 and $\vdash s : S$ and $\llbracket T/S \rrbracket \subseteq Q$; and the goal is to prove $t/s \Downarrow^{\text{co}} Q$.

823 CASE 1: the term t has been typed using rule `TYP-RAND`. In this case, the term t has the form
 824 “`rand t_1` ”, and T is `int`. The rule concludes $S; \emptyset \vdash (\text{rand } t_1) : \text{int}$, from the premise $S; \emptyset \vdash t_1 : \text{int}$
 825 and the premise $t_1 \in \mathbb{V}$. Because t_1 typechecks in the empty environment, it must be a value.
 826 Because this value has type `int`, it must be an integer value, let us call it n . We need to establish:
 827 $(\text{rand } n)/s \Downarrow^{\text{co}} Q$. We apply the rule `CO-OMNI-BIG-RAND-COMPLETE`, which is like `OMNI-BIG-RAND-`
 828 `COMPLETE` but part of the coinductive interpretation of the set of evaluation rules. We need to prove
 829 its premise: $\forall m. 0 \leq m < \max(n, 1) \Rightarrow (m, s) \in Q$. Consider a particular m in that range. We have
 830 $\llbracket \text{int}/S \rrbracket \subseteq Q$. Thus, to show $(m, s) \in Q$ it suffices to show $(m, s) \in \llbracket \text{int}/S \rrbracket$. By definition of the
 831 operator $\llbracket T/S \rrbracket$, this amounts to proving $\exists S' \supseteq S. (\vdash s : S') \wedge (S' \vdash m : \text{int})$. We conclude by
 832 taking $S' = S$ and checking that $\vdash s : S$ and $S' \vdash m : \text{int}$ indeed hold.

834 CASE 2: the term t has been typed using rule `TYP-LET`. In this case, the term t has the form
 835 “let $x = t_1$ in t_2 ”. The rule concludes $S; \emptyset \vdash (\text{let } x = t_1 \text{ in } t_2) : T$, from the two premises $S; \emptyset \vdash t_1 : T_1$
 836 and $S; (x : T_1) \vdash t_2 : T$. We need to establish: $(\text{let } x = t_1 \text{ in } t_2)/s \Downarrow^{\text{co}} Q$. We apply the rule `CO-OMNI-`
 837 `BIG-LET` (which is like `OMNI-BIG-LET` but part of the coinductive interpretation of the set of evaluation
 838 rules) with Q_1 instantiated as $\llbracket T_1/S \rrbracket$. We have to establish the two premises: $t_1/s \Downarrow \llbracket T_1/S \rrbracket$, and
 839 $\forall (v', s') \in \llbracket T_1/S \rrbracket. ([v'/x] t_2)/s' \Downarrow Q$. The first premise follows directly from the coinduction
 840 hypothesis applied to $S; \emptyset \vdash t_1 : T_1$ and to $\llbracket T_1/S \rrbracket \subseteq \llbracket T_1/S \rrbracket$. For the second premise, consider a pair
 841 $(v', s') \in \llbracket T_1/S \rrbracket$. This amounts to assuming the existence of some S' such that $S' \supseteq S$ and $\vdash s' : S'$
 842 and $S' \vdash v : T_1$. There remains to show $([v'/x] t_2)/s' \Downarrow Q$. A standard “type preservation upon
 843 store typing extension” lemma shows that, because $S' \supseteq S$, we can refine $S; (x : T_1) \vdash t_2 : T$ into
 844 $S'; (x : T_1) \vdash t_2 : T$. Then, by the standard substitution lemma applied to $S'; (x : T_1) \vdash t_2 : T$
 845 and to $S' \vdash v : T_1$, we derive $S'; \emptyset \vdash ([v'/x] t_2) : T$. Besides, the lemma `CONFIGURATION-TYPING-`
 846 `SUBSET` applied to $S' \supseteq S$ gives $\llbracket T/S' \rrbracket \subseteq \llbracket T/S \rrbracket$. Composing this subset relation by transitivity with
 847 $\llbracket T/S \rrbracket \subseteq Q$ yields $\llbracket T/S' \rrbracket \subseteq Q$. The conclusion $([v'/x] t_2)/s' \Downarrow Q$ then follows from the coinduction
 848 hypothesis applied to $S'; \emptyset \vdash ([v'/x] t_2) : T$ and $\vdash s' : S'$ and $\llbracket T/S' \rrbracket \subseteq Q$.

849 Note that most of these arguments are easily handled by automated proof search in Coq. \square

850

851

852 Like for the small-step settings, we proved once-and-for-all that the statement `COINDUCTION-`
 853 `FOR-TYPE-SAFETY` entails `TYPE-SAFETY`.

854

855 Our coinductive omni-big-step approach offers, to those who have good reasons to work with a
 856 big-step-style semantics, a means to establish type safety without introducing error rules.

857

858 Regarding the comparison with the standard preservation-and-progress approach, at this stage
 859 we cannot draw general conclusions on whether omni-big-step and omni-small-step type-safety
 860 proofs are more effective, because we considered a relatively simple language. Nevertheless, it
 861 appears that each of the two approaches that we propose results in proof scripts that (1) require
 862 only one induction or one coinduction instead of two separate inductions, (2) are no longer than
 863 with preservation and progress separated, and (3) avoid the issue of nested inversions requiring a
 864 number of cases quadratic in the size of the language.

865 5 DEFINITION OF PROGRAM PROOF RULES

866 This section discusses the construction of a *foundational* program logic, that is, a program logic
 867 whose reasoning rules are derived through mechanized proofs from the formal semantics of the
 868 targeted programming language. We specifically focus on Separation Logic [O’Hearn et al. 2001;
 869 Reynolds 2002], which has proved in the past two decades to be an invaluable tool for carrying
 870 out practical, modular program verification, both for low-level and high-level languages—see the
 871 broad survey by O’Hearn [2019] and the survey by Charguéraud [2020] that focuses on sequential
 872 programs.

873 We first review the properties that a program logic might capture, and we describe the key
 874 challenges in deriving a foundational Separation Logic that captures total correctness with respect
 875 to a standard big-step semantics (§5.1). We then explain how omnismantics overcome these
 876 challenges, allowing one to derive a foundational, total-correctness Separation Logic judgment in
 877 a straightforward, direct manner (§5.2). Moreover, by referring to the coinductive omni-big-step
 878 judgment instead of the inductive one, one can similarly define partial-correctness triples. We
 879 explain how to derive the reasoning rules (§5.3) and in particular the frame rule of Separation
 880 Logic (§5.4). We also present reasoning rules in weakest-precondition style (§5.5), which turns out
 881 to be even easier to derive. Finally, we present *bind rules* for reasoning about evaluation contexts
 882 and thereby handling programs that are not in A-normal form (§5.6).

882

5.1 Challenges in Defining Foundational Separation Logic Triples

A Hoare triple, written $\{H\} t \{Q\}$, describes the behavior of the evaluation of the configurations t/s for any s satisfying the precondition H , in terms of the postcondition Q . The exact interpretation of a triple depends on whether it accounts for *total correctness* or *partial correctness*, which differ on how they account for termination. For nondeterministic languages, the key notions of interest for defining a triple $\{H\} t \{Q\}$ are as follows.

- **Safety:** for any s satisfying H , none of the possible evaluations of t/s can get stuck.
- **Correctness:** for any s satisfying H , if t/s can evaluate to v/s' , then $Q v s'$ holds.
- **Termination:** for any s satisfying H , all possible evaluations of t/s are finite.
- **Partial correctness:** safety and correctness hold.
- **Total correctness:** safety, correctness, and termination hold.

Most foundational program logics target partial correctness, e.g. [Cao et al. 2018; Chlipala 2013; Jung et al. 2018; Ni and Shao 2006]. Fewer projects target total correctness. Prior to the introduction of omnisemantics, we are aware of work by Guéneau et al. [2017] on the CakeML framework [Kumar et al. 2014]. That work provides a foundational approach to CFML’s characteristic formulae [Charguéraud 2011]. The construction of foundational characteristic formulae was subsequently revisited and simplified by Charguéraud [2021]. In those pieces of work, the underlying semantics considered are either deterministic or *deterministic up to the choice of memory addresses* at allocation points.

When targeting total correctness, one key challenge in defining triples with respect to a standard big-step semantics is that the direct definition of Hoare triples yields a judgment that does not satisfy the *frame rule* of Separation Logic. The frame rule asserts that if a triple $\{H\} t \{Q\}$ holds, then the pre- and the postcondition may be extended with an arbitrary predicate H' , yielding the valid triple $\{H \star H'\} t \{Q \star H'\}$. Here, $Q \star H$ denotes the postcondition $\lambda v. (Q v \star H)$.

Concretely, consider the following definition of a Hoare triple with respect to a standard big-step, deterministic semantics. It asserts that, for any input state s satisfying the precondition H , there exists a result value v and a final state s' such that the configuration t/s evaluates to a final configuration v/s' that satisfies the postcondition Q .

$$\text{Hoare } \{H\} t \{Q\} \equiv \forall s. H s \Rightarrow \exists v. \exists s'. (t/s \Downarrow v/s') \wedge (Q v s').$$

For such a judgment, one can prove that, starting from an empty heap, the allocation of a reference returns a *specific* memory location, say 0. For example, if the reference contains 3 and the location l denotes its address, one can prove: $\text{Hoare } \{[]\} (\text{ref } 3) \{\lambda l. [l = 0] \star (0 \hookrightarrow 3)\}$. To see why the judgment does not satisfy the frame rule, let us attempt to extend the pre- and the postcondition of this triple with the heap predicate $(0 \hookrightarrow 1)$, which denotes a reference at location 0 storing the value 1. We obtain: $\text{Hoare } \{0 \hookrightarrow 1\} (\text{ref } 3) \{\lambda l. [l = 0] \star (0 \hookrightarrow 3) \star (0 \hookrightarrow 1)\}$. This triple does not hold, because the separating conjunction $(0 \hookrightarrow 3) \star (0 \hookrightarrow 1)$ is equivalent to False.

To derive a Separation Logic judgment that *does* satisfy the frame rule, one can exploit the classic technique of the *baked-in frame rule* [Birkedal et al. 2005]—for technical and historical details, we refer to Charguéraud [2020, §5.1 and §10.2]. Separation Logic triples are defined as follows.

$$\text{Sep. Logic } \{H\} t \{Q\} \equiv \forall H'. \text{Hoare } \{H \star H'\} t \{Q \star H'\}$$

This definition quantifies over a heap predicate H' that describes the “rest of the world.” The resulting triples inherently satisfy the frame rule, as a direct consequence of the associativity of the separating-conjunction operator. Indirectly, the introduction of H' rules out the judgments whose postconditions refer to *specific* locations, such as in the aforementioned counterexample.

The two-stage construction presented above, for building Separation Logic triples on top of the standard big-step judgment via the *baked-in frame rule* technique, can be applied to deterministic

languages or to languages that are deterministic up to the choice of memory addresses. In what follows, we show that, by grounding Separation Triples not on top of standard big-step semantics but instead on top of omnismantics, we can avoid the need to go through the two-stage construction associated with the *baked-in frame rule* technique. Moreover, the omnismantics construction applies to the general case of *nondeterministic* semantics, and it unfolds similarly for both total- and partial-correctness triples.

5.2 Definition of Hoare Triples w.r.t. Omni-Big-Step Semantics

Consider a possibly nondeterministic semantics. A total-correctness Hoare triple $\{H\} t \{Q\}$ asserts that, for any input state s satisfying the precondition H , every possible execution of t/s terminates and satisfies the postcondition Q . This property can be captured using the *inductive* omni-big-step judgment as follows:

$$\text{total, nondeterministic } \{H\} t \{Q\} \equiv \forall s. H s \Rightarrow (t/s \Downarrow Q)$$

Note that an omni-big-step judgment may be interpreted as a particular Hoare triple, featuring a singleton precondition to constrain the input state:

$$(t/s \Downarrow Q) \iff \text{total, nondeterministic } \{(\lambda s'. s' = s)\} t \{Q\}.$$

A partial-correctness Hoare triple asserts that, for any input state s satisfying the precondition H , every possible execution of t/s either diverges or terminates and satisfies the postcondition. This property can be captured using the *coinductive* omni-big-step judgment as follows:

$$\text{partial, nondeterministic } \{H\} t \{Q\} \equiv \forall s. H s \Rightarrow (t/s \Downarrow^{\text{co}} Q)$$

Note that instantiating Q with the always-false predicate in the partial-correctness triple yields a characterization of programs whose execution always diverges—and never gets stuck.

Throughout the rest of this section, we present results for total correctness. All the corresponding results for partial correctness hold and may be found in our Coq formalization.

5.3 Deriving Reasoning Rules for Hoare Triples

In a foundational program logic, reasoning rules take the form of lemmas proved correct with respect to the definition of triples and with respect to the semantics of the language. Consider for example the case of a let-binding. Let us compare the semantics rule OMNI-BIG-LET with the Hoare-logic rule HOARE-LET, which are shown below. Throughout this section, we formulate rules by viewing postconditions as predicates of type $\text{val} \rightarrow \text{state} \rightarrow \text{Prop}$, as this presentation style is more idiomatic in program logics. We also present reasoning rules using the horizontal bar, but keep in mind that the statements are not inductive definitions but lemmas.

$$\begin{array}{c} \text{OMNI-BIG-LET} \\ \frac{t_1/s \Downarrow Q_1 \quad (\forall v' s'. Q_1 v' s' \Rightarrow ([v'/x] t_2)/s' \Downarrow Q)}{(\text{let } x = t_1 \text{ in } t_2)/s \Downarrow Q} \\ \text{HOARE-LET} \\ \frac{\{H\} t_1 \{Q_1\} \quad (\forall v'. \{Q_1 v'\} ([v'/x] t_2) \{Q\})}{\{H\} (\text{let } x = t_1 \text{ in } t_2) \{Q\}} \end{array}$$

The only difference between OMNI-BIG-LET and HOARE-LET is that the first rule considers one specific state s , whereas the second rule considers a set of possible states satisfying the precondition H . By exploiting the fact that $\{H\} t \{Q\}$ is defined as $\forall s. H s \Rightarrow (t/s \Downarrow Q)$, it is straightforward to establish that HOARE-LET is a consequence of OMNI-BIG-LET. The corresponding Coq proof script witnesses the simplicity of the proof: “intros. apply mbig_let; eauto.”

As another example, consider the consequence rule. The Hoare-logic rule is, again, an immediate consequence of the omni-big-step rule.

$$\begin{array}{c} \text{OMNI-BIG-CONSEQUENCE} \\ \frac{t/s \Downarrow Q \quad Q \subseteq Q'}{t/s \Downarrow Q'} \\ \text{HOARE-CONSEQUENCE} \\ \frac{H' \subseteq H \quad \{H\} t \{Q\} \quad Q \subseteq Q'}{\{H'\} t \{Q'\}} \end{array}$$

5.4 Deriving The Frame Rule of Separation Logic

We next explain how to derive the frame rule for total-correctness triples. To that end, we first need to state and prove a key lemma capturing the preservation of the omni-big-step judgment $t/s_1 \Downarrow Q$ when the input state s_1 is augmented with a disjoint piece of state s_2 . We write $s_1 \perp s_2$ to assert that s_1 and s_2 have disjoint domains.

LEMMA 5.1 (FRAME PROPERTY FOR BIG-STEP OMNISEMANTICS).

$$\frac{t/s_1 \Downarrow Q \quad s_1 \perp s_2}{t/(s_1 \uplus s_2) \Downarrow (Q \star (\lambda s'. s' = s_2))} \text{OMNI-BIG-FRAME}$$

PROOF. The proof is carried out by induction on the omnisemantics judgment. There are two interesting cases in the proof: the treatment of an allocation (4 lines of Coq script) and that of a let-binding (3 lines of Coq script). In each case, we assume $s_1 \perp s_2$.

CASE 1: t is $\text{ref } v$. The assumption is $(\text{ref } v)/s_1 \Downarrow Q$. It is derived by the rule OMNI-BIG-REF, whose premise is $\forall p \notin \text{dom } s_1. Qp(s_1[p := v])$. We need to prove $(\text{ref } v)/(s_1 \uplus s_2) \Downarrow (Q \star (\lambda s'. s' = s_2))$. By OMNI-BIG-REF, we need to justify: $\forall p \notin \text{dom } (s_1 \uplus s_2). (Q \star (\lambda s'. s' = s_2))p((s_1 \uplus s_2)[p := v])$. Consider a location p not in $\text{dom } s_1$ nor in $\text{dom } s_2$. The predicate $(Q \star (\lambda s'. s' = s_2))p$ is equivalent to $(Qp) \star (\lambda s'. s' = s_2)$. The state update $(s_1 \uplus s_2)[p := v]$ is equivalent to $(s_1[p := v]) \uplus s_2$. Thus, there remains to prove: $((Qp) \star (\lambda s'. s' = s_2))((s_1[p := v]) \uplus s_2)$. By definition of separating conjunction and exploiting $(s_1[p := v]) \perp s_2$, it suffices to show $Qp(s_1[p := v])$. This fact follows from $\forall p \notin \text{dom } s_1. Qp(s_1[p := v])$.

CASE 2: t is “let $x = t_1$ in t_2 ”. The assumption is $t/s_1 \Downarrow Q$. It is derived by the rule OMNI-BIG-LET, whose premises are $t_1/s_1 \Downarrow Q_1$ and $\forall v's'. Q_1 v's' \Rightarrow ([v'/x] t_2)/s' \Downarrow Q$. We need to prove $(\text{let } x = t_1 \text{ in } t_2)/(s_1 \uplus s_2) \Downarrow (Q \star (\lambda s'. s' = s_2))$. To that end, we invoke OMNI-BIG-LET. For its first premise, we prove $t_1/(s_1 \uplus s_2) \Downarrow (Q_1 \star (\lambda s'. s' = s_2))$ by exploiting the induction hypothesis applied to $t_1/s_1 \Downarrow Q_1$. For the second premise, we have to prove $\forall v's''. (Q_1 \star (\lambda s'. s' = s_2))v's'' \Rightarrow ([v'/x] t_2)/s'' \Downarrow (Q \star (\lambda s'. s' = s_2))$. Consider a particular v' and s'' . The assumption $(Q_1 \star (\lambda s'. s' = s_2))v's''$ is equivalent to $((Q_1 v') \star (\lambda s'. s' = s_2))s''$. By definition of separating conjunction, we deduce that s'' decomposes as $s'_1 \uplus s_2$, with $s'_1 \perp s_2$ and $Q_1 v's'_1$, for some s'_1 . There remains to prove $([v'/x] t_2)/(s'_1 \uplus s_2) \Downarrow (Q \star (\lambda s'. s' = s_2))$. We first exploit $\forall v's'. Q_1 v's' \Rightarrow ([v'/x] t_2)/s' \Downarrow Q$, on $Q_1 v's'_1$ to obtain $([v'/x] t_2)/s'_1 \Downarrow Q$. We then conclude by applying the induction hypothesis to the latter judgment. \square

LEMMA 5.2 (FRAME RULE).

$$\frac{\{H\} t \{Q\}}{\{H \star H'\} t \{Q \star H'\}} \text{FRAME} \quad \text{where } Q \star H \equiv \lambda v. (Qv \star H)$$

PROOF. Assume $\{H\} t \{Q\}$. Recall from §5.2 that this judgment is defined as $\forall s. Hs \Rightarrow (t/s \Downarrow Q)$. We have to prove $\{H \star H'\} t \{Q \star H'\}$, that is, $\forall s. (H \star H')s \Rightarrow (t/s \Downarrow (Q \star H'))$. Consider a particular s such that $(H \star H')s$. By definition of separating conjunction, we can deduce that the input state s decomposes as $s_1 \uplus s_2$, with $s_1 \perp s_2$ and Hs_1 and $H's_2$. The goal is to prove: $t/(s_1 \uplus s_2) \Downarrow (Q \star H')$. By exploiting $\forall s. Hs \Rightarrow (t/s \Downarrow Q)$ on Hs_1 , we derive $t/s_1 \Downarrow Q$. By invoking the lemma

1030 OMNI-BIG-FRAME on this judgment and on $s_1 \perp s_2$, we derive $t/(s_1 \uplus s_2) \Downarrow (Q \star (\lambda s'. s' = s_2))$.
 1031 From there, to obtain the conclusion $t/(s_1 \uplus s_2) \Downarrow (Q \star H')$, it suffices to exploit the consequence
 1032 rule OMNI-BIG-CONSEQUENCE, and justify that $(\lambda s'. s' = s_2)$ entails H' . In other words, we need to
 1033 show that for any state s' being equal to s_2 , this state s' does satisfy H' . Indeed, $H' s_2$ holds. (The
 1034 Coq proof script for this lemma is 4 lines long.) \square

1035

1036 5.5 Deriving Weakest-Precondition-Style Reasoning Rules

1037 The weakest-precondition operator, written $\text{wp } t \ Q$, computes the weakest predicate H for which
 1038 the triple $\{H\} t \{Q\}$ holds. Here, “weakest” is interpreted w.r.t. the entailment relation, written
 1039 $H \vdash H'$ and defined as pointwise predicate implication ($\forall s. H s \Rightarrow H' s$). Weakest reasoning rules
 1040 are expressed in the form of entailments, e.g., the rule for let-bindings is as follows.

1041

WP-LET

1042

$$\frac{}{\text{wp } t_1 (\lambda v'. \text{wp } ([v'/x] t_2) Q) \vdash \text{wp } (\text{let } x = t_1 \text{ in } t_2) Q}$$

1044

1045 Many proof tools simply axiomatize the weakest-precondition rules. In a *foundational* approach,
 1046 however, one needs to prove the reasoning rules correct with respect to the formal semantics of
 1047 the source language.

1048 What is very appealing about describing the semantics of the language using an omni-big-step
 1049 semantics is that it delivers the weakest-precondition-style reasoning rules almost for free. Indeed,
 1050 the interpretation of the inductive judgment $t/s \Downarrow Q$ matches, up to the order of arguments, the
 1051 standard interpretation of the weakest-precondition operator.

1052

$$\text{wp } t \ Q \ s \iff t/s \Downarrow Q$$

1053

1054 Thus, in a foundational approach, we can formally define $\text{wp } t \ Q$ as $\lambda t \ Q \ s. (t/s \Downarrow Q)$.

1055 There remains to describe how the weakest-precondition-style reasoning rules can be derived
 1056 from the omni-big-step evaluation rules. Doing so is even easier than for deriving triples. Consider
 1057 for example the semantics rule and the wp-reasoning rule associated with a let-binding.

1058

$$\frac{t_1/s \Downarrow Q_1 \quad (\forall v' s'. Q_1 v' s' \Rightarrow ([v'/x] t_2)/s' \Downarrow Q)}{(\text{let } x = t_1 \text{ in } t_2)/s \Downarrow Q} \text{OMNI-BIG-LET}$$

1060

1061 To derive the rule WP-LET from OMNI-BIG-LET, it suffices to instantiate Q_1 as $\lambda v_1. \text{wp } ([v_1/x] t_2) Q$.

1062

1063 The frame rule in weakest-precondition style follows directly from the OMNI-BIG-FRAME lemma
 1064 established in the previous section. The rule appears below, together with a very handy corollary
 1065 named the *ramified frame rule* [Hobor and Villard 2013; Krishnaswami et al. 2010]. In that corollary,
 1066 the magic wand between postconditions, written $Q_1 \multimap Q_2$, is defined as $\forall v. Q_1 v \multimap Q_2 v$, where \forall
 1067 and \star are the standard Separation Logic operators (see, e.g., [Charguéraud 2020, §3.2 and §7]).

1068

WP-FRAME

WP-RAMIFIED-FRAME

1069

$$\frac{}{(\text{wp } t \ Q) \star H \vdash \text{wp } t \ (Q \star H)}$$

$$\frac{}{(\text{wp } t \ Q) \star (Q \multimap Q') \vdash (\text{wp } t \ Q')}$$

1070

1071 For most other term constructs, the wp rule is nothing but a copy of the omni-big-step rule
 1072 with arguments reordered. One interesting exception is that of loops. “While” loops have not
 1073 been discussed so far, but they appear in the language used for the case studies in §6. Typically,
 1074 standard weakest-precondition rules for while loops are stated using loop invariants. In contrast, an
 1075 omni-big-step rule essentially unfolds the first iteration of the loop, just like in a standard big-step
 1076 semantics. From that unfolding rule, one can derive the loop-invariant-based rule by induction, in
 1077 just a few lines of proof.

1078

In summary, by considering a semantics expressed in omni-big-step style, one can derive practical reasoning rules, both in Hoare-triple style and in weakest-precondition style, in most cases via one-line proofs. The construction of a program logic on top of an omni-big-step semantics is thus a significant improvement, both over the use of a standard big-step semantics, which falls short in the presence of nondeterminism; and over the use of a small-step semantics, which requires much more work for deriving the reasoning rules, especially if one aims for total correctness. Besides, a major benefit of considering an omni-big-step semantics is that, unlike a set of weakest-precondition reasoning rules, it delivers an induction principle for reasoning about program executions. Such induction principles are exploited in the case studies (§6).

5.6 The Bind Rule for Reasoning about Evaluation Contexts

In this section, we explain how to reason about programs that are not in A-normal form. We follow the approach of the *bind rule*, popularized by Iris [Jung et al. 2018] in the context of program logics. The bind rule follows the pattern of the let-binding rule but allows for evaluation of a subterm t that appears in an *evaluation context* E .

For the syntax introduced in §2.1 and used so far, we can define evaluation contexts by the following grammar, where \square denotes the *hole*, i.e., the empty context.

$$E ::= \square \mid \text{let } x = E \text{ in } t \mid (E t) \mid (v E) \mid \text{if } E \text{ then } t \text{ else } t$$

We write $E[t]$ for the context E whose hole is filled with the term t . We write $\text{value } t$ for the predicate that asserts that t is a value. The bind rule describes how to evaluate or reason about subterms that appear in evaluation contexts and that are not already values. The omni-big-step bind rule takes the following form.

$$\frac{\neg \text{value } t \quad t/s \Downarrow Q_1 \quad (\forall v s'. Q_1 v s' \Rightarrow E[v]/s' \Downarrow Q)}{E[t]/s \Downarrow Q} \text{OMNI-BIG-BIND}$$

The premise $\neg \text{value } t$ could be omitted for the inductive interpretation of the omni-big-step rules. It is required, however, for the coinductive interpretation, to prevent the construction of infinite derivations for terms that do not diverge.

The corresponding reasoning rules, expressed using either triples or weakest preconditions, appear next. Observe that these two rules need not include a premise of the form $\neg \text{value } t$. Indeed, the rules remain valid even in the case where t is already a value.

$$\frac{\text{HOARE-BIND} \quad \{H\} t \{Q_1\} \quad (\forall v. \{Q_1 v\} E[v] \{Q\})}{\{H\} E[t] \{Q\}} \quad \frac{\text{WP-BIND}}{\text{wp } t (\lambda v. \text{wp } (E[v]) Q) \vdash \text{wp } (E[t]) Q}$$

6 COMPILER-CORRECTNESS PROOFS FOR TERMINATING PROGRAMS

Omnisemantics also simplify some of the characteristic complexities of behavior-preservation proofs for compilers.

6.1 Motivation: Avoiding Both Backward Simulations and Artificial Determinism

Following CompCert's terminology [Leroy 2009], one particular evaluation of a program can admit one out of four possible behaviors: *terminate* (with a value, an exception, a fatal error, etc.), trigger *undefined behavior*, *diverge silently* after performing a finite number of I/O operations, or be *reactive* by performing an infinite sequence of I/O operations. Whether an error such as a division by zero is considered as a terminating behavior or as an undefined behavior is a design decision associated

1128 with each programming language. A general-purpose compiler ought to preserve behaviors, except
1129 that undefined behaviors can be replaced with anything.

1130 In this paper, we focus on proofs of compiler correctness for programs that always terminate
1131 safely. Such a result is sufficient for many practical applications in software verification where
1132 source programs are proven to be safe, and often, the only use case for nontermination is a top-level
1133 infinite event-handling loop, which can be implemented in assembly language [Erbsen et al. 2021].
1134 We leave to future work the application of omniseantics to the correct compilation of programs
1135 that diverge, react, or trigger undefined behavior on some inputs but not others.

1136 In the particular case of a deterministic programming language, compiler correctness for ter-
1137 minating programs can be established via a *forward-simulation* proof.⁷ Such a proof consists of
1138 showing that each step from the source program *corresponds to* a number of steps in the compiled
1139 program. The correspondence involved is captured by a relation between source states and target
1140 states. Such forward-simulation proofs work well in practice. The main problem is that they do not
1141 generalize to nondeterministic languages.

1142 Indeed, in the presence of nondeterminism, a source program may have several possible execu-
1143 tions. As we restrict ourselves to the case of terminating programs, let us assume that all executions
1144 of the source program terminate, only possibly with different results. In that setting, a compiler
1145 is correct if (1) the compiled program always terminates, and (2) for any result that the compiled
1146 program may produce, the source program could have produced that result. It may not be intuitive
1147 at first, but the inclusion is indeed *backwards*: the set of behaviors of the target program must be
1148 included in the set of behaviors of the source program.

1149 To establish the backward behavior inclusion, one may set up a *backward-simulation* proof. Such
1150 a proof consists of showing that each step from the target program corresponds to one or more
1151 steps in the source program.⁸ Yet, backward simulations are much more unwieldy to set up than
1152 forward simulations. Indeed, in most cases one source-program step is implemented by multiple
1153 steps in the compiled program, thus a backward-simulation relation typically needs to relate many
1154 more pairs than a forward-simulation relation.

1155 This observation motivated the CompCert project [Leroy 2009] to exploit forward simulations
1156 as much as possible, at the cost of modeling features of the intermediate language as deterministic
1157 even when it is not natural to do so, and even when doing so requires introducing artificial functions
1158 for, e.g., computing fresh memory locations in a deterministic manner. Even so, runtime input
1159 does not fit the fully deterministic model, leading to the technical definitions of receptiveness and
1160 determinacy (roughly, capturing the idea of determinism modulo input) so that lemmas for flipping
1161 forward simulations into backwards simulations can be stated and proven. Omniseantics remove
1162 the need for this machinery.

1163 In this section:

- 1164 • We explain how omniseantics sidestep the need for backward simulations, by carrying
1165 out forward-simulation proofs of compiler correctness, for nondeterministic terminating
1166 programs.
- 1167 • We show how the idea generalizes to languages including I/O operations and to the case
1168 where the source language and target language are different.

1170 ⁷We follow CompCert’s terminology, using “forward” and “backward” to refer to the direction of compilation, “forward”
1171 meaning from source language to target language. We note the conflict with other literature [Lynch and Vaandrager 1995]
1172 that uses “forward” and “backward” to refer to the direction of the state transitions.

1173 ⁸The number of corresponding steps in the source program should not be zero, otherwise the target program could diverge
1174 whereas the source program terminates. In practice, however, it is not always easy to find one source-program step that
1175 corresponds to a target-program step. In such situations, one may consider a generalized version of backward simulations
1176 that allow for zero source-program steps, provided that some well-founded measure decreases [Leroy 2009].

- We present two case studies: one transformation that increases the amount of nondeterminism and one that decreases the amount of nondeterminism.
- We comment on the fact that our second case study features an omni-big-step semantics for the source language, whereas it features an omni-small-step semantics for the target language.

6.2 Establishing Correctness via Forward Simulations using Omnisemantics

Consider a compilation function written $C(t)$. For simplicity, we assume that the source and target language are identical, we assume that compilation does not alter the result values, and we assume the language to be state-free—we will generalize the results in the next subsection. In this subsection, $t \Downarrow v$ denotes the standard big-step judgment, $t \Downarrow Q$ denotes the omni-big-step judgment, and $\text{terminates}(t)$ asserts that all executions of t terminate safely, without undefined behavior. The compiler-correctness result for terminating programs captures preservation of termination and backward inclusion for results—points (1) and (2) stated earlier.

CORRECTNESS-FOR-TERMINATING-PROGRAMS:

$$\text{terminates}(t) \Rightarrow \text{terminates}(C(t)) \wedge (\forall v. C(t) \Downarrow v \Rightarrow t \Downarrow v)$$

We claim that this correctness result can be derived from the following statement, which describes forward preservation of specifications.

$$\text{OMNI-FORWARD-PRESERVATION: } \forall Q. t \Downarrow Q \Rightarrow C(t) \Downarrow Q$$

Let us demonstrate the claim. Let us assume that $\text{terminates}(t)$ hold. First of all, recall from §2.2 the equivalence named OMNI-BIG-STEP-IFF-TERMINATES-AND-CORRECT that relates the omni-big-step judgment and the termination judgment.

$$t \Downarrow Q \iff \text{terminates}(t) \wedge (\forall v. (t \Downarrow v) \Rightarrow v \in Q)$$

Exploiting this equivalence, the OMNI-FORWARD-PRESERVATION assumption reformulates as follows.

$$\begin{aligned} \forall Q. & \quad (\text{terminates}(t) \wedge (\forall v. (t \Downarrow v) \Rightarrow v \in Q)) \\ & \Rightarrow (\text{terminates}(C(t)) \wedge (\forall v. (C(t) \Downarrow v) \Rightarrow v \in Q)) \end{aligned}$$

The hypothesis $\text{terminates}(t)$ holds by assumption. Let us instantiate Q as the strongest post-condition for t , that is, as the set $\{v \mid (t \Downarrow v)\}$. We obtain:

$$(\forall v. (t \Downarrow v) \Rightarrow (t \Downarrow v)) \Rightarrow \text{terminates}(C(t)) \wedge (\forall v. (C(t) \Downarrow v) \Rightarrow (t \Downarrow v)).$$

The premise is a tautology, and the conclusion proves CORRECTNESS-FOR-TERMINATING-PROGRAMS.

6.3 Omnisemantics Simulations for I/O and Cross-Language Compilation

More generally, the behavior of a terminating program consists of the final result and its interactions with the outside world (input and output). These interactions include, e.g., interaction with the standard input and output streams, system calls, etc. Each interaction is called an *event*, and the semantics judgment is extended to collect such events into a *trace* τ . Figure 5 shows three illustrative cases of how the rules from Figure 2 are augmented with traces, making the choice to treat rand calls as observable events while reference-allocation nondeterminism remains internal.

Requiring a compiler to preserve only the nondeterministic choices recorded in the trace enables us to pick and choose which (external) interactions must be preserved by compilations and which (internal) nondeterministic choices the compiler may resolve as it sees fit. As a particularly fine-grained example, the trace might record that `malloc` was called and succeeded but omit the pointer it returned, to allow for optimizations that reduce the amount of allocation. To our

$$\begin{array}{c}
1226 \quad \text{OMNI-BIG-LET-TRACE} \\
1227 \quad \frac{t_1/s/\tau \Downarrow Q_1 \quad (\forall v', s', \tau' \in Q_1. ([v'/x] t_2)/s'/\tau' \Downarrow Q)}{(\text{let } x = t_1 \text{ in } t_2)/s/\tau \Downarrow Q} \\
1228 \\
1229 \\
1230 \quad \text{OMNI-BIG-RAND-TRACE} \quad \text{OMNI-BIG-REF} \\
1231 \quad \frac{n > 0 \quad (\forall m. 0 \leq m < n \Rightarrow (m, s, (n, m) :: \tau) \in Q)}{(\text{rand } n)/s/\tau \Downarrow Q} \quad \frac{\forall p \notin \text{dom } s. (p, s[p := v], \tau) \in Q}{(\text{ref } v)/s/\tau \Downarrow Q} \\
1232 \\
1233
\end{array}$$

Fig. 5. Omni-big-step semantics with traces, selected rules

1234
1235
1236 knowledge, this level of flexibility is unique to omnisemantics. For a forward-simulation-based
1237 compiler-correctness proof, constructing a deterministic model of all internal nondeterminism can
1238 be arbitrarily complicated (the CompCert memory model is an example).

1239 We restrict our attention to semantics that only accept terminating commands c that do not go
1240 wrong and do not return values, for the rest of this section. For languages of terms (that return
1241 values) rather than commands (that do not return values), we would need a representation relation
1242 between source-level and target-level values—we omit one here for brevity, but §6.4 tackles a similar
1243 challenge. In the current setting, *behavior inclusion* holds between a source-language program and
1244 a target-language program if all traces that the target-language program can produce (according to
1245 traditional small-step or big-step semantics) can also be produced by the source-language program.
1246 More formally, we define the traces that can be produced from a starting configuration $c/s/\tau$ as

$$1247 \quad \text{traces}(c, s, \tau) := \{\tau' \mid \exists s'. c/s/\tau \Downarrow s'/\tau'\}$$

1248
1249 and say that a compiler C satisfies behavior inclusion for a command starting from the initial
1250 source-level state s_{src} related to the initial target-level state s_{tgt} and initial trace τ if TRACEINCLUSION
1251 as defined below holds.

$$1252 \quad \text{TRACEINCLUSION}(c, s_{src}, s_{tgt}, \tau) := \text{traces}(C(c), s_{tgt}, \tau) \subseteq \text{traces}(c, s_{src}, \tau)$$

1253
1254 Assuming omni-big-step semantics \Downarrow_{src} and \Downarrow_{tgt} for the source and target languages, plus a
1255 relation R between source- and target-language states, we define *omnisemantics simulation*, a
1256 compiler-correctness criterion designed to be provable by induction on the \Downarrow_{src} judgment, as
1257 follows:

$$1258 \quad \text{OMNISEMANTICSSIMULATION}(c) := \forall s_{src} s_{tgt} \tau Q. R(s_{src}, s_{tgt}) \wedge c/s_{src}/\tau \Downarrow_{src} Q \\
1259 \quad \implies C(c)/s_{tgt}/\tau \Downarrow_{tgt} Q_R \\
1260 \quad \text{where } Q_R(s'_{tgt}, \tau') := \exists s'_{src}. R(s'_{src}, s'_{tgt}) \wedge Q(s'_{src}, \tau')$$

1261
1262 Our goal in this section is to prove that an omnisemantics simulation implies trace inclusion if
1263 the source program terminates, i.e. to show

$$1264 \quad \forall c. \text{OMNISEMANTICSSIMULATION}(c) \implies \\
1265 \quad \forall s_{src} s_{tgt} \tau. \text{terminates}(c, s_{src}, \tau) \wedge R(s_{src}, s_{tgt}) \implies \text{TRACEINCLUSION}(c, s_{src}, s_{tgt}, \tau)$$

1266 We rely on two properties: First, soundness of omni-big-step semantics with respect to traditional
1267 big-step semantics:

$$1268 \quad \forall c s s' \tau \tau' Q. c/s/\tau \Downarrow s'/\tau' \wedge c/s \Downarrow Q \implies Q(s', \tau') \quad (1)$$

1269
1270 And conversely, that a program that terminates safely and whose traditional big-step executions
1271 all satisfy a postcondition also has an omnisemantics derivation:

$$1272 \quad \forall c s \tau Q. \text{terminates}(c, s, \tau) \wedge (\forall s' \tau'. c/s/\tau \Downarrow s'/\tau' \implies Q(s', \tau')) \implies c/s/\tau \Downarrow Q \quad (2)$$

1275 To show trace inclusion, i.e. $\text{traces}(C(c), s_{tgt}, \tau) \subseteq \text{traces}(c, s_{src}, \tau)$, we can assume a target-
 1276 language execution $C(c)/s_{tgt}/\tau \Downarrow s'_{tgt}/\tau'$ producing trace τ' , and we need to show $\tau' \in \text{traces}(c, s_{src}, \tau)$.
 1277 By applying (2) to the source program (whose termination we assume) and setting $Q(s'_{src}, \tau') :=$
 1278 $c/s_{src}/\tau \Downarrow s'_{src}/\tau'$ so that the second premise becomes a tautology, we obtain the source-level omnise-
 1279 mantics derivation $c/s_{src}/\tau \Downarrow (\lambda s'_{src}. \tau'. c/s_{src}/\tau \Downarrow s'_{src}/\tau')$. Passing this fact into the omnisemantics
 1280 simulation yields $C(c)/s_{tgt}/\tau \Downarrow (\lambda s'_{tgt}. \tau'. \exists s'_{src}. R(s'_{src}, s'_{tgt}) \wedge c/s_{src}/\tau \Downarrow s'_{src}/\tau')$. Now we can apply (1)
 1281 to this fact and the originally assumed target-level execution and obtain an s'_{src} such that $R(s'_{src}, s'_{tgt})$
 1282 and $c/s_{src}/\tau \Downarrow s'_{src}/\tau'$, which by definition is exactly what needs to hold to have $\tau' \in \text{traces}(c, s_{src}, \tau)$.
 1283

1284 6.4 Case Study: Compiling Immutable Pairs to Heap-Allocated Records

1285 This section describes a simple compiler pass that *increases* the amount of nondeterminism. The
 1286 source language assumes a primitive notion of tuples, whereas the target language encodes such
 1287 tuples by means of heap allocation. This case study is formalized with respect to a language based
 1288 on commands whose arguments all must be variables. Such a language could be an intermediate
 1289 language in a compiler pipeline, reached after an expression-flattening phase.

1290 *Language syntax.* We let c denote a command, x , y , and z denote identifiers, and n denote
 1291 unbounded natural-number constants. The grammar of the language is as follows.

1292
$$c := x = \text{unop}(y) \mid x = \text{binop}(y, z) \mid x = \text{input}() \mid \text{output}(x) \mid x = y[n] \mid x[n] = y \mid$$

 1293
$$x = \text{alloc}(n) \mid x = n \mid x = y \mid c_1; c_2 \mid \text{if } x \text{ then } c_1 \text{ else } c_2 \mid \text{while } x \text{ do } c \mid \text{skip}$$

1294 We actually consider two variants of this language, differing only in the types of values and in
 1295 the available unary operators *unop* and binary operators *binop*. The source language features an
 1296 inductively defined type of values that can be natural numbers n or immutable pairs of values (i.e.,
 1297 the grammar of values is $v := n \mid (v, v)$). It includes as unary operators the projection functions *fst*
 1298 and *snd* (defined only on pairs) and the Boolean negation *not* (defined only on $\{0, 1\}$). Its binary
 1299 operators are addition (+) and pair creation *mkpair*. The target language admits only natural
 1300 numbers as values. It includes only the negation and addition operators.
 1301

1302 *Omni-big-step semantics.* For both languages, the omni-big-step evaluation judgment takes the
 1303 form $c/m/\ell/\tau \Downarrow Q$, where c is a command, m is a memory state (a partial map from natural numbers
 1304 to natural numbers), ℓ is an environment of local variables (a partial map from identifiers to values,
 1305 whose type differs between the source and target languages as described above), τ denotes the
 1306 I/O trace made of the events already performed *before* executing c , and the postcondition Q is
 1307 a predicate over triples of the form (m', ℓ', τ') . A trace consists of a list of I/O events e whose
 1308 grammar is $e := \text{IN } n \mid \text{OUT } n$.
 1309

1310 The rules defining the judgment appear in Figure 6. They are common to both languages—only
 1311 the set of supported unary and binary operators differs. The semantics of operators are defined
 1312 by two straightforward auxiliary relations (spelled out in Appendix H), $\text{evalunop}(\text{unop}, v_1, v_2)$
 1313 asserting that applying *unop* to value v_1 results in v_2 , and $\text{evalbinop}(\text{binop}, v_1, v_2, v_3)$ asserting
 1314 that applying *binop* to v_1 and v_2 results in v_3 . The *load* command $x = y[n]$ requires that the local
 1315 variable y contains a natural number a and stores the value of the memory at address $a + n$ into
 1316 variable x (and is undefined if $a + n$ is not mapped by the memory). The *store* command $x[n] = y$
 1317 stores the natural number contained in the local variable y at memory location $a + n$, where a is
 1318 the address contained in local variable x , but only if memory at address $a + n$ has already been
 1319 allocated.

1320 The command $x = \text{input}()$ reads a natural number n , stores it into local variable x , and adds
 1321 the event (IN n) to the event trace. The number n is chosen nondeterministically but recorded in
 1322 the trace, resulting in *external* nondeterminism. The language has a built-in memory allocator but,
 1323

<p>1324 EVAL-UNOP</p> <p>1325 $(y, v_y) \in \ell \quad \text{evalunop}(op, v_y, v)$</p> <p>1326 $\frac{Q(m, \ell[x := v], \tau)}{(x = op(y))/m/\ell/\tau \Downarrow Q}$</p>	<p>1325 EVAL-STORE</p> <p>1326 $(x, a) \in \ell \quad (a + n) \in \text{dom } m$</p> <p>1327 $\frac{(y, v) \in \ell \quad Q(m[(a + n) := v], \ell, \tau)}{(x[n] = y)/m/\ell/\tau \Downarrow Q}$</p>
<p>1329 EVAL-INPUT</p> <p>1330 $\forall n. Q(m, \ell[x := n], \tau :: \text{IN } n)$</p> <p>1331 $\frac{(x = \text{input}())}{(x = \text{input}())/m/\ell/\tau \Downarrow Q}$</p>	<p>1329 EVAL-ALLOC</p> <p>1330 $(\forall a \bar{v}. \text{len}(\bar{v}) = n \wedge a, \dots, (a + n - 1) \notin \text{dom } m$</p> <p>1331 $\implies Q(m[(a, \dots, (a + n - 1)) := \bar{v}], \ell[x := a], \tau)$</p> <p>1332 $\frac{}{(x = \text{alloc}(n))/m/\ell/\tau \Downarrow Q}$</p>
<p>1334 EVAL-WHILE-AGAIN</p> <p>1335 $(x, 1) \in \ell \quad c/m/\ell/\tau \Downarrow Q_1$</p> <p>1336 $\frac{(\forall m' \ell' \tau'. Q_1(m', \ell', \tau') \implies (\text{while } x \text{ do } c)/m'/\ell'/\tau' \Downarrow Q)}{(\text{while } x \text{ do } c)/m/\ell/\tau \Downarrow Q}$</p>	<p>1334 EVAL-WHILE-DONE</p> <p>1335 $(x, 0) \in \ell \quad Q(m, \ell, \tau)$</p> <p>1336 $\frac{}{(\text{while } x \text{ do } c)/m/\ell/\tau \Downarrow Q}$</p>
<p>1339 EVAL-SEQ</p> <p>1340 $\frac{c_1/m/\ell/\tau \Downarrow Q_1 \quad (\forall m' \ell' \tau'. Q_1(m', \ell', \tau') \implies c_2/m'/\ell'/\tau' \Downarrow Q)}{(c_1; c_2)/m/\ell/\tau \Downarrow Q}$</p>	

Fig. 6. Nondeterministic omni-big-step semantics for an imperative language (selected rules)

for simplicity, we do not deal with deallocation. The command $x = \text{alloc}(n)$ nondeterministically picks an address (natural number) a such that a , as well as the $n - 1$ addresses following a , are not yet part of the memory, initializes these addresses with nondeterministically chosen values, and returns a . This rule encodes *internal* nondeterminism, because this action is not recorded in the event trace. Semantics of while loops are given by sequencing the first iteration with the loop itself as long as the loop test succeeds.

In practice, we found it convenient also to derive a chained version EVAL-SEQ-CHAINED of the omni-big-step rule EVAL-SEQ, just like we did for omni-small-step rules in §3.2.

$$\text{EVAL-SEQ-CHAINED} : \quad c_1/m/\ell/\tau \Downarrow (\lambda m' \ell' \tau'. (c_2/m'/\ell'/\tau' \Downarrow Q)) \implies (c_1; c_2)/m/\ell/\tau \Downarrow Q$$

Note that the chained variant cannot be used to define the judgement inductively in Coq due to the *strict positivity* requirement; more details on encoding choices can be found in Appendix A.

Compilation function. The compilation function C lays out the pairs of the source language on the heap memory of the target language. This function is defined recursively on the source program. It maps the source-language operators that are not supported by the target language as follows.

$$\begin{aligned} C(x = \text{fst}(y)) &:= x = y[0] \\ C(x = \text{snd}(y)) &:= x = y[1] \\ C(x = \text{mkpair}(y, z)) &:= \text{tmp} = \text{alloc}(2); \text{tmp}[0] = y; \text{tmp}[1] = z; x = \text{tmp} \end{aligned}$$

Note that to compile `mkpair`, we cannot simply store the address returned by `alloc` directly into x , because if x is the same variable name as y or z , then we would be overwriting the argument. For this reason, we use a temporary variable `tmp` that we declare to be reserved for compiler usage.

Simulation relation. To carry out the proof of correctness of the function $C(c)$, we introduce a simulation relation R for relating a source-language state (m_1, ℓ_1) with a target-language state (m_2, ℓ_2) . To that end, we first define the relation `valuerepr`(v, w, m), to relate a source-language value v with the corresponding target-language value w , in a target-language memory m . This

relation is implemented as the recursive function shown below—it could equally well consist of an inductive definition. A pair (v_1, v_2) is represented by address w if recursively v_1 is represented by the value at address w , and v_2 is represented by the value at address $w + 1$. A natural number n has the same representation on the target-language level, i.e. we just assert $w = n$.

$$\begin{aligned} \text{valuerepr}((v_1, v_2), w, m) &:= (\exists w_1. (w, w_1) \in m \wedge \text{valuerepr}(v_1, w_1, m)) \wedge \\ &\quad (\exists w_2. (w + 1, w_2) \in m \wedge \text{valuerepr}(v_2, w_2, m)) \\ \text{valuerepr}(n, w, m) &:= w = n \end{aligned}$$

The relationship R between source and target states can then be defined using `valuerepr`. In the definition shown below, we write $m_2 \supseteq m_1$ to mean that memory m_2 extends m_1 , and we write $m_2 \setminus m_1$ to denote the map-subtraction operator that restricts m_2 to contain only addresses not bound in m_1 . The locations bound by m_2 but not by m_1 correspond to the memory addresses of the pairs allocated on the heap in the target language.

$$\begin{aligned} R((m_1, \ell_1), (m_2, \ell_2)) &:= \text{tmp} \notin \text{dom } \ell_1 \wedge m_2 \supseteq m_1 \wedge \\ &\quad \forall (x, v) \in \ell_1. \exists w. (x, w) \in \ell_2 \wedge \text{valuerepr}(v, w, m_2 \setminus m_1) \end{aligned}$$

Correctness proof. We are now ready to tackle the omni-forward-simulation proof.

THEOREM 6.1 (OMNISEMANTICS SIMULATION FOR THE PAIR-HEAPIFICATION COMPILER).

$$\begin{aligned} \forall c \ m_{src} \ \ell_{src} \ m_{tgt} \ \ell_{tgt} \ \tau \ Q. \ \text{tmp} \notin \text{vars}(c) \wedge R((m_{src}, \ell_{src}), (m_{tgt}, \ell_{tgt})) \wedge \\ c/m_{src}/\ell_{src}/\tau \Downarrow_{src} Q \implies \\ C(c)/m_{tgt}/\ell_{tgt}/\tau \Downarrow_{tgt} Q_R \end{aligned}$$

$$\text{where } Q_R(m'_{tgt}, \ell'_{tgt}, \tau') := \exists m'_{src} \ \ell'_{src}. R((m'_{src}, \ell'_{src}), (m'_{tgt}, \ell'_{tgt})) \wedge Q(m'_{src}, \ell'_{src}, \tau')$$

PROOF. By induction on the derivation of $c/m_{src}/\ell_{src}/\tau \Downarrow Q$. In each case, the goal to prove is initially of the form $C(c)/m_{tgt}/\ell_{tgt}/\tau \Downarrow Q_R$, where c has some structure that allows us to simplify $C(c)$ into a more concrete program snippet. We consider the resulting simplified goal as an invocation of a weakest-precondition generator on that program snippet, and we view the rules of Figure 6 as weakest-precondition rules that we can apply in order to step through the program snippet, using the hypotheses obtained from inverting the source-level derivation $c/m_{src}/\ell_{src}/\tau \Downarrow Q$ to discharge the side conditions that arise. Whenever we encounter a sequence of commands, we use `EVAL-SEQ-CHAINED` instead of `EVAL-SEQ`, so that we do not have to provide an intermediate postcondition. In the cases where commands have subcommands, we use the inductive hypotheses about their execution as if they were previously proven lemmas about these “functions.”

We only present the case where $c = (x = \text{mkpair}(y, z))$ in more detail: We have to prove a goal of the form $C(x = \text{mkpair}(y, z))/m_{tgt}/\ell_{tgt}/\tau \Downarrow Q_R$, which simplifies to

$$(\text{tmp} = \text{alloc}(2); \text{tmp}[0] = y; \text{tmp}[1] = z; x = \text{tmp})/m_{tgt}/\ell_{tgt}/\tau \Downarrow Q_R$$

Applying `EVAL-SEQ-CHAINED` turns it into:

$$(\text{tmp} = \text{alloc}(2))/m_{tgt}/\ell_{tgt}/\tau \Downarrow (\lambda m'_{tgt} \ \ell'_{tgt} \ \tau'. (\text{tmp}[0] = y; \text{tmp}[1] = z; x = \text{tmp})/m'_{tgt}/\ell'_{tgt}/\tau' \Downarrow Q_R)$$

Applying `EVAL-ALLOC` turns it into:

$$\begin{aligned} \forall a \ \bar{v}. \text{len}(\bar{v}) = 2 \implies a, a + 1 \notin \text{dom } m_{tgt} \implies \\ (\text{tmp}[0] = y; \text{tmp}[1] = z; x = \text{tmp})/m_{tgt}[a..(a + 1) := \bar{v}]/\ell_{tgt}[\text{tmp} := a]/\tau \Downarrow Q_R \end{aligned}$$

Note how the fact that the address a and the list of initial values \bar{v} are chosen nondeterministically naturally shows up as a universal quantification, and note how the memory and locals appearing in the state to the left of the \Downarrow have been updated by the `alloc` function. After introducing these universally quantified variables and the hypotheses, we again have a goal of the form “... \Downarrow ...”

and continue by applying EVAL-SEQ-CHAINED, EVAL-STORE, EVAL-SEQ-CHAINED, EVAL-STORE, EVAL-SET. Finally, we prove Q_R for the locals and memory updated according to the various EVAL-... rules that we applied by using map laws and the initial hypothesis $R((m_{src}, \ell_{src}), (m_{tgt}, \ell_{tgt}))$. \square

6.5 Case Study: Introduction of Stack Allocation

This second case study illustrates the case of a transformation that reduces the amount of nondeterminism. The transformation consists of adding a *stack-allocation* feature to the compiler developed by Erbsen et al. [2021]. Proving this transformation correct using an omni-big-step forward simulation was straightforward and took us only a few days of work—most of the work was *not* concerned with dealing with nondeterminism. This smooth outcome is in stark contrast to the outlook of using traditional evaluation judgments: verifying the same transformation would have required either more complex invariants, to set up a backward simulation; or completely rewriting the memory model so that pointers are represented by deterministically generated unobservable identifiers, to allow for a compiler-correctness proof by forward simulation. In fact, addressable stack allocation was initially omitted from the language exactly to avoid these intricacies (based on the experience from CompCert), but switching to omnisemantics made its addition local and uncomplicated.

The input language is an imperative command language similar to the one described in §6.4. The memory is modeled as a partial map from machine words (32-bit or 64-bit integers) to bytes. The stack-allocation feature here consists of a command `let $x = \text{stackalloc}(n)$ in c` made available in the source language. This command assigns an address to variable x at which n bytes of memory will be available during the execution of command c . Our compiler extension implements this command by allocating the requested n bytes on the stack, computing the address at runtime based on the stack pointer.

The key challenge is that the source-language semantics does not feature a stack. The stack gets introduced further down the compilation chain. Thus, the simplest way to assign semantics to the `stackalloc` function in the source language is to pretend that it allocates memory at a *nondeterministically chosen* memory location. This nondeterministic choice is described using a universal quantification in the omni-big-step rule shown below, like in rule OMNI-BIG-REF from §2.

$$\frac{\forall m_{new} a. (\text{dom } m_{new} \cap \text{dom } m) = \emptyset \wedge \text{dom } m_{new} = [a, a + n] \implies c / (m \cup m_{new}) / \ell [x := a] / \tau \Downarrow \lambda m' \ell' \tau'. P(m' \setminus m_{new}, \ell', \tau')}{(\text{let } x = \text{stackalloc}(n) \text{ in } c) / m / \ell / \tau \Downarrow P} \text{ OMNI-BIG-STACKALLOC}$$

In the source language, the address returned by `stackalloc` is picked nondeterministically, whereas in the target language the address used for the allocation is deterministically computed, as the current stack pointer augmented with some offset. Thus, the compiler phase that compiles away the `stackalloc` command *reduces* the amount of nondeterminism.

Compiler-correctness proof. The compiler-correctness proof proceeds by induction on the omnisemantics derivation for the source language, producing a target-language execution with a related postcondition. The simulation relation R describes the target-language memory as a disjoint union of unallocated stack memory and the source-language memory state. Critically, the case for `stackalloc` has access to a universally quantified induction hypothesis (derived from the rule shown above) about *target-level executions of $C(c)$ for any address a* .

As the address of the stack-allocated memory is not recorded in the trace, we are free to instantiate it with the specific stack-space address, expressed in terms of compile-time stack-layout parameters and the runtime stack pointer. Reestablishing the simulation relation to satisfy the precondition of that induction hypothesis then involves carving out the freshly allocated memory from unused stack space and considering it a part of the source-level memory instead, matching the compiler-chosen

1471 memory layout and the preconditions of the stackalloc omnisemantics rule. It is this last part that
1472 made up the vast majority of the verification work in this case study; handling the nondeterminism
1473 itself is as straightforward as it gets.

1474 Note that it would not be possible to complete the proof by instantiating a with a compiler-
1475 chosen offset from the stack pointer if the semantics recorded the value of a in the trace. The
1476 (unremarkable) proof for the input command in the previous section also has access to a universally
1477 quantified execution hypothesis, but it *must* directly instantiate its universally quantified induction
1478 hypothesis with the variable introduced when applying the target-level omnisemantics input rule
1479 to the goal, to match the target-language trace to the source-language trace. Either way, reasoning
1480 about the reduction of nondeterminism in an omni-forward-preservation proof boils down to
1481 instantiating a universal quantifier.

1482
1483 *Design decisions around proving absence of out-of-memory.* In the verified software-hardware stack
1484 described in Erbsen et al. [2021], the main bottleneck in terms of complexity that prevents us from
1485 developping bigger applications is the program logic used to verify source programs. Therefore,
1486 we made an effort to avoid adding more proof obligations in the program logic whenever possible.
1487 At the same time, for the targeted application it was fine to limit the expressivity of the source
1488 language. In particular, we decided that disallowing recursive calls is acceptable. Given that setting,
1489 we want to avoid reasoning about out-of-memory conditions in the source language, while still
1490 proving that the *compiled* program will not run out of memory, which we can achieve as follows.

1491 In the OMNI-BIG-STACKALLOC rule of our source language, we deliberately use a vacuous universal
1492 quantification if we run out of memory, because we prefer to handle out-of-memory conditions
1493 outside of the omnisemantics judgment, in an additional external judgment. In particular, this
1494 means that if OMNI-BIG-STACKALLOC is applied with a memory m whose domain already contains
1495 all (or almost all) addresses (which are 32-bit or 64-bit words), there might be no m_{new} and a such
1496 that the left-hand side of the implication above the line in OMNI-BIG-STACKALLOC holds, so we can
1497 derive any postcondition P , something that we cautioned against in §2.2.

1498 Effectively, this means that our source-language evaluation rules do not guarantee that the
1499 program never runs out of memory. This choice simplifies the program-logic proofs for concrete
1500 input programs but requires additional work in the compiler: the compiler performs a simple
1501 static-analysis pass over the call graph of the program to determine the maximum amount of stack
1502 space that the program needs. Since this analysis rejects recursive calls, the space upper bound is
1503 not hard to compute. The compiler-correctness proof contains an additional hypothesis requiring
1504 that at least that computed amount of memory is available in the state on which the target-language
1505 program begins its execution.

1506 An alternative approach would be to introduce a notion of “amount of used stack space” in the
1507 source-language semantics and include an additional precondition in the OMNI-BIG-STACKALLOC
1508 rule that requires this amount to be bounded. This approach would put more complexity into the
1509 verification of source programs, while simplifying the compiler correctness proof. In order to allow
1510 recursive calls and dynamically chosen stack-allocation sizes, reasoning about the amount of stack
1511 space in the program logic seems to become unavoidable, in which case this alternative approach
1512 would be preferable.

1513 6.6 Compilation from a Language in Omni-Big-Step to One in Omni-Small-Step

1514
1515 If the semantics of the source language of a compiler phase are most naturally expressed in omni-
1516 big-step, but the target language’s semantics are best expressed in omni-small-step semantics, it is
1517 convenient to prove an omni-forward simulation directly from a big-step source execution to a
1518 small-step target execution. For instance, the compiler in the project by Erbsen et al. [2021] includes
1519

such a translation, relating a big-step intermediate language to a small-step assembly language. In fact, this translation happens in the same case study described in the previous subsection. In what follows, we attempt to give a flavor of the proof obligations that arise from switching from omni-big-step to omni-small-step during the correctness proof.

Consider one sample omni-small-step rule, for the load-word instruction `lw` that loads the value at the address stored in register r_2 and assigns it to register r_1 :

$$\frac{\text{ASM-LW} \quad (pc, lw \ r_1 \ r_2) \in m \quad (r_2, a) \in rf \quad (a, v) \in m \quad P(m, rf[r_1 := v], pc + 1, \tau)}{m/rf/pc/\tau \longrightarrow P}$$

Here, we model a machine state s_{tgt} as a quadruple of a memory m (that contains both instructions and data), a register file rf mapping register names to machine words, a program counter pc , and a trace τ . One can prove an omni-forward simulation from big-step source semantics directly to small-step target semantics:

$$\forall s_{src} \ s_{tgt} \ P. R(s_{src}, s_{tgt}) \wedge s_{src} \Downarrow P \implies s_{tgt} \longrightarrow^\diamond (\lambda s'_{tgt}. \exists s'_{src}. R(s'_{src}, s'_{tgt}) \wedge P(s'_{src}))$$

where R asserts, among other conditions, that the memory of the target state s_{tgt} contains the compiled program.

Like the proof described in §6.4, this proof also works by stepping through the target-language program by applying target-language rules and discharging their side conditions using the hypotheses obtained by inverting the source-language execution, with the only difference that instead of using the derived big-step rule `EVAL-SEQ-CHAINED` for chaining, one now uses the following two rules: `EVENTUALLY-STEP-CHAINED` and `EVENTUALLY-CUT`.

Applying `EVENTUALLY-STEP-CHAINED` turns the goal into an omni-single-small-step goal with a given postcondition, which is suitable to discharge using rules with universally quantified postconditions like `ASM-LW`. Applying `EVENTUALLY-CUT`, on the other hand, creates two subgoals containing an uninstantiated unification variable for the intermediate postcondition. The unification variable appears as the postcondition in the first subgoal, so an induction hypothesis with the concrete postcondition from the theorem statement can be applied. In the second subgoal, this postcondition becomes the precondition for the remainder of the execution.

7 RELATED AND FUTURE WORK

This work builds on that of Schäfer et al. [2016], Charguéraud [2020], and Erbsen et al. [2021], all of which are discussed in the introduction (§1). We will review some additional connections.

Relationship to coinductive big-step semantics. Leroy and Grall [2009] argue that fairly complex, optimizing compilation passes can be proved correct more easily using big-step semantics than using small-step semantics. These authors propose to reason about diverging executions using *coinductive big-step semantics*, following up on an earlier idea by Cousot and Cousot [1992]. Leroy and Grall's judgment, written $t/s \uparrow^{\text{co}}$, asserts that there exists a diverging execution of t/s . This judgment is defined coinductively, and a number of its rules refer to the standard big-step judgment. For example, consider the two rules associated with divergence of a `let`-binding. An expression `let $x = t_1$ in t_2` diverges either because t_1 diverges (rule `DIV-LET-1`) or because t_1 terminates on a value v_1 and the term $[v_1/x] t_2$ diverges (rule `DIV-LET-2`).

$$\frac{t_1/s \uparrow^{\text{co}}}{(\text{let } x = t_1 \text{ in } t_2)/s \uparrow^{\text{co}}} \text{DIV-LET-1} \qquad \frac{t_1/s \Downarrow v_1/s' \quad ([v_1/x] t_2)/s' \uparrow^{\text{co}}}{(\text{let } x = t_1 \text{ in } t_2)/s \uparrow^{\text{co}}} \text{DIV-LET-2}$$

In contrast, the coinductive omni-big-step judgment involves a single rule, namely CO-OMNI-BIG-LET, defined as part of the coinductive interpretation of the rules from Fig. 2.

$$\frac{t_1/s \Downarrow^{\text{co}} Q_1 \quad \left(\forall (v_1, s') \in Q_1. ([v_1/x] t_2)/s' \Downarrow^{\text{co}} Q \right)}{(\text{let } x = t_1 \text{ in } t_2)/s \Downarrow^{\text{co}} Q} \text{CO-OMNI-BIG-LET}$$

In that rule, if Q_1 is instantiated as the empty set, the second premise becomes vacuous, and we recover the rule DIV-LET-1. Otherwise, if Q_1 is nonempty, then it describes the values v_1 that t_1 may evaluate to. For each possible value v_1 , the second premise of the rule requires the term $[v_1/x] t_2$ to diverge, just like in the rule DIV-LET-2. In summary, CO-OMNI-BIG-LET captures at once the logic of both DIV-LET-1 and DIV-LET-2.

The paper by Leroy and Grall [2009], which focuses on a deterministic semantics, points out that the principle of excluded middle (classical logic) is required for establishing the equivalence between a *coinductive big-step semantics* for divergence and the *standard small-step semantics* for divergence. Interestingly, classical logic is *not* required for establishing the equivalence between a *coinductive omni-big-step semantics* of divergence and the standard small-step semantics for divergence. In the explanations that follow, we omit the state for simplicity, and we write $t \longrightarrow_{\text{co}}^{\infty}$ for the standard small-step divergence judgment, defined as $\forall t'. (t \longrightarrow^* t') \Rightarrow \exists t''. (t' \longrightarrow t'')$.

The implication that requires classical logic to be established is: $(t \longrightarrow_{\text{co}}^{\infty}) \Rightarrow (t \Uparrow^{\text{co}})$. To see why, consider a term t of the form $\text{let } x = t_1 \text{ in } t_2$, where t_1 corresponds to a program whose termination is an open mathematical problem, and where t_2 is an infinite loop. Thus, no matter whether t_1 diverges or not, the program $\text{let } x = t_1 \text{ in } t_2$ diverges. Yet, to establish the judgment $(\text{let } x = t_1 \text{ in } t_2) \Uparrow^{\text{co}}$, one needs to know whether t_1 diverges, in which case the rule DIV-LET-1 applies, or whether t_1 terminates, in which case the rule DIV-LET-2 applies. In the general case, one has to invoke the excluded middle to decide on the termination of an abstract term t_1 .

In contrast, the implication $(t \longrightarrow_{\text{co}}^{\infty}) \Rightarrow (t \Downarrow^{\text{co}} \emptyset)$, which targets a coinductive omni-big-step semantics, can be proved without classical logic, as pointed out earlier in §3.4. Intuitively, to prove that the same example program $\text{let } x = t_1 \text{ in } t_2$ diverges, one can apply the rule CO-OMNI-BIG-LET, regardless of whether t_1 diverges or not. It suffices to instantiate Q_1 , which denotes the set of possible results of t_1 , as the strongest postcondition of t_1 . The strongest postcondition may be expressed in terms of the omni-big-step judgment (recall §2.2), or equivalently in terms of the small-step judgment by instantiating Q_1 as $\{v_1 \mid t_1 \longrightarrow^* v_1\}$. In particular, if t_1 diverges, then the set Q_1 is empty and the second premise of CO-OMNI-BIG-LET becomes vacuous. What matters for the proof of equivalence between the small-step semantics and the coinductive omni-big-step semantics is that we do not need to *decide* whether Q_1 is empty, i.e., whether t_1 diverges or not. We thereby avoid the need for the excluded middle.

Semantics of nondeterministic programs. An important aspect of the present paper is the set up of semantics for nondeterministic language constructs. Let us review the key historical papers that have focused on that task. Nondeterminism appears in the early work on semantics, including the language of guarded commands of Dijkstra [1976] that admits nondeterministic choice where guards overlap, and the par construct of Milner [1975]. Plotkin [1976] develops a *powerdomain* construction to give a fully abstract model in which equivalences such as $(p \text{ par } q) = (q \text{ par } p)$ hold. Francez et al. [1979] also present semantics that map each program to a representation of the set of its possible results. In all these presentations, nondeterminism is *bounded*: only a finite number of choices are allowed.

Subsequent work generalizes the powerdomain interpretation to *unbounded nondeterminism*. For example, Back [1983] considers a language construct $x := \epsilon P$ that assigns x to an arbitrary value satisfying the predicate P —the program has undefined behavior if no such value exists. Apt and

Plotkin [1986] address the lack of *continuity* in the models presented in earlier work, still leveraging the notion of powerdomains. Their presentation includes a (countable) nondeterministic assignment operator, written $x := ?$, that sets x to an arbitrary integer in \mathbb{Z} . More recent work by Tassarotti et al. [2017] heavily relies on the bounded nondeterminism assumption in an extension of Iris [Jung et al. 2018] for developing a logic to justify program refinement. These authors speculate that *transfinite step-indexing* [Schwinghammer et al. 2013; Svendsen et al. 2016] may allow handling unbounded nondeterminism.

Coinductive characterization of safety. Wang et al. [2014] define a safety judgment, written $\text{safe}(t, s)$, to assert that all possible executions of the configuration t/s execute safely, i.e., do not get stuck. To reason in big-step style, and to avoid the cumbersome introduction of error-propagation rules, they consider a coinductive definition. We reproduce below the rule for let-bindings, which reads as follows: to establish that $\text{let } x = t_1 \text{ in } t_2$ executes safely, prove that t_1 executes safely and that, for any possible result v_1 produced by t_1 , the result of the substitution $[v_1/x] t_2$ executes safely.

$$\frac{\text{safe}(t_1, s) \quad (\forall v_1 s'. (t_1/s \Downarrow v_1/s') \Rightarrow \text{safe}([v_1/x] t_2, s'))}{\text{safe}(\text{let } x = t_1 \text{ in } t_2, s)} \text{SAFE-LET (COINDUCTIVE)}$$

Our judgment $t/s \Downarrow^{\text{co}} Q$ generalizes the notion of safety, by baking the postcondition directly into the judgment (§2.4). It asserts not only that t/s cannot get stuck but also that any potential final configuration belongs to Q . We formalized in Coq the following equivalence.

OMNI-CO-BIG-STEP-IFF-SAFE-AND-CORRECT :

$$t/s \Downarrow^{\text{co}} Q \iff \text{safe}(t, s) \wedge (\forall v s'. (t/s \Downarrow v/s') \Rightarrow (v, s') \in Q)$$

Our rule OMNI-BIG-LET extends SAFE-LET not just by adding the postcondition Q to the judgment but also by changing the quantification over v_1/s' . In the rule SAFE-LET, the quantification is constrained by $t_1/s \Downarrow v_1/s'$, whereas in the rule OMNI-BIG-LET, it is constrained by $(v_1, s') \in Q_1$, where Q_1 denotes the postcondition of t_1/s . The key innovation here is that, thanks to the introduction of postconditions, we no longer need to refer to the standard big-step judgment—the judgment $t/s \Downarrow Q$ gives a stand-alone definition of the semantics of the language.

Semantics of reactive programs. One key question is how much of a program’s internal nondeterminism should be reflected in its *execution trace*. At one extreme, one could include a *delay* event, a.k.a. a *tick*, to reflect in the trace each transition performed by the program, following the approaches of Danielsson [2012]. More recent work on interaction trees [Koh et al. 2019; Xia et al. 2019] maps each program to a coinductive structure featuring ticks in addition to I/O steps. Yet, these approaches come at the cost of reasoning “up to removal of a finite number of ticks.”

A promising route to avoiding ticks is the *mixed inductive-coinductive* approach of Nakata and Uustalu [2010], for distinguishing between *reactive* programs that always eventually perform I/O operations and *silently diverging* programs that eventually continue executing forever without performing any I/O. Despite apparent benefits, this approach seems not to have gained popularity or evaluation in the form of sizable case studies. It would be interesting future work to investigate whether a mixed inductive-coinductive version of omnisemantics can be defined and provide smooth reasoning for the combined challenge of potentially infinite executions, nondeterminism, and undefined behavior. The key challenge is to find a way to carry out compiler-correctness proofs through a single pass that handles reasoning about both terminating and nonterminating executions. We are also looking forward to future work on omnisemantics that could provide new approaches to reasoning about divergence and reactivity without counting ticks.

1667 *Compiler correctness as trace property preservation.* Abate et al. [2021] define the notion of *source*
1668 *trace property preservation* (denoted $TP^{\bar{r}}$) to mean that all properties that hold on traces produced
1669 by the source program also hold on traces produced by the target program. They allow different
1670 trace formats in the source and target language, relating the source trace s to a target trace t by a
1671 relation $s \sim t$ and quantifying over them in the same way as we quantify over the source and target
1672 states in the definition of *omnisemantics simulation* (§6.3). If we instantiate the definition of Abate
1673 et al. [2021] by traces whose single events stand for emitting final states, we obtain our definition
1674 of omnisemantics simulation, and vice versa, if we generalize our definition to also allow different
1675 trace formats but omit the state component, we obtain their definition. However, including the state
1676 component in our definition makes it directly usable for a forward-style proof by induction on the
1677 source-language derivation, even in the presence of target-language nondeterminacy. We speculate
1678 that several proofs of example compilers in that paper could be revisited using omnisemantics.
1679 Doing so would not only simplify the proofs but also make the results stronger by removing the
1680 target-language determinacy assumption, which they need to derive backward simulations from
1681 forward simulations.

1682
1683 *Semantics of concurrent programs.* Concurrency increases the amount of nondeterminism, due
1684 to interleavings, and generally increases the sources of undefined behaviors, due in particular to
1685 data races. The work on CompCertTSO [Ševčík et al. 2013] shows how to deal with this additional
1686 complexity in a compiler-correctness proof. A direction for future work is to investigate the extent
1687 to which omni-small-step semantics would help simplify proofs from CompCertTSO.

1688 The Iris framework [Jung et al. 2018, 2015] supports reasoning about concurrent programs
1689 in Separation Logic. In Iris, the source language is specified by means of a traditional small-step
1690 semantics. The weakest precondition predicate is then defined using *step-indexing*: one first defines
1691 the notion of “a program is well-behaved for n steps” by induction over n ; then defines the notion of
1692 “a program is well-behaved” as “it is well-behaved for any number of steps”. Proofs are then typically
1693 carried out by induction over the indices. Yet, the indices involved get in the way of compiler proofs
1694 where the number of computation steps may increase or decrease through a transformation. This
1695 observation motivated the introduction of more advanced techniques to tame the issue, such as
1696 transfinite step-indexing [Svendsen et al. 2016]. When reasoning about *sequential* programs, the
1697 use of step-indexing appears overkill for most applications. By leveraging an inductive definition
1698 of the weakest precondition predicate, one obtains a direct induction principle that avoids the
1699 technicalities and limitations of step-indexing altogether.

1700
1701 *Semantics of probabilistic programs.* Probabilistic semantics aim to describe not just which ex-
1702 ecutions are possible but also to describe with what probability each execution may happen. A
1703 probabilistic small-step execution relation assigns a probability to every transition. One caveat is
1704 that probabilities do not suffice to describe all nondeterminism: in particular, memory is allocated at
1705 nondeterministically chosen addresses. We refer to Batz et al. [2019] for a solution to this challenge.
1706 In the context of program logics, McIver and Morgan [2005] introduce a *weakest preexpectation*
1707 *calculus*. Batz et al. [2019] generalize this notion to set up a *Quantitative Separation Logic*.

1708 Additionally, there is a long line of work aiming at providing denotational models for probabilistic
1709 programs—e.g., Staton et al. [2016]; Wang et al. [2019]. Denotational and operational semantics
1710 serve different purposes; one important practical benefit of omnisemantics is that it is grounded
1711 in inductive definitions, with respect to which proofs by induction can be carried out easily in a
1712 proof assistant. An interesting question is whether omnisemantics could be adapted to provide an
1713 inductively defined operational semantics that accounts for probabilities, by relating configurations
1714 not to sets of outcomes but instead to probability distributions of outcomes.

1715

1716 The problem of termination of probabilistic programs is particularly subtle. On the one hand,
1717 one may be interested in capturing that any execution terminates. For example, [Staton et al. \[2016\]](#)
1718 define termination as “there exists n , such that termination occurs in n steps.” However, this
1719 approach does not apply to infinitely branching nondeterminism. On the other hand, one may
1720 design rules to establish *almost-sure termination* or *positive-almost-sure termination* [[Chakarov and](#)
1721 [Sankaranarayanan 2013](#); [Ferrer Fioriti and Hermanns 2015](#); [Kaminski et al. 2016](#); [McIver et al. 2017](#)].

1722 *Dijkstra monads.* Dijkstra monads [[Ahman et al. 2017](#); [Maillard et al. 2019](#)] target code written
1723 in monadic form and specified using dependent types. The type-checking process essentially
1724 applies weakest-precondition rules and results in the production of proof obligations. In practice,
1725 specifications are expressed in first-order logic, so that proof obligations can be discharged using
1726 SMT solvers. Dijkstra monads encourage metareasoning using object-language dependent types
1727 only; they do not appear to have been designed for, or demonstrated capable of, carrying out
1728 inductions over program executions. Dijkstra monads can be instantiated in particular with the
1729 nondeterminism monad (NDet). In the current presentation [[Ahman et al. 2017](#)], the monad models
1730 sets of possible outcomes using finite sets, which rules out infinitely branching nondeterminism
1731 and does not allow for abstraction in intermediate postconditions (e.g., asserting that a subterm t_1
1732 returns an even integer).
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1912 A ON THE CHALLENGE OF DEFINING WP INDUCTIVELY

1913 The weakest-precondition-style reasoning rule for let-bindings is traditionally stated as follows.

$$1914 \quad \text{WP-LET:} \quad \text{wp } t_1 (\lambda v'. \text{wp } ([v'/x] t_2) Q) \vdash \text{wp } (\text{let } x = t_1 \text{ in } t_2) Q.$$

1916 Translating it to a big-step omnisemantics rule results in the following rule.

$$1917 \quad \frac{t_1/s \Downarrow \{(v', s') \mid ([v'/x] t_2)/s' \Downarrow Q\}}{(\text{let } x = t_1 \text{ in } t_2)/s \Downarrow Q} \text{ OMNI-BIG-LET-CHAINED}$$

1921 The rule OMNI-BIG-LET-CHAINED can be useful for reasoning when one does not want to specify an explicit postcondition that needs to hold between t_1 and t_2 . This *chained* rule can be straightforwardly derived from the OMNI-BIG-LET rule part of the definition of the omni-big-step semantics, by instantiating Q_1 as $\{(v', s') \mid ([v'/x] t_2)/s' \Downarrow Q\}$ in the first premise, then checking the tautology associated with the second premise.

$$1922 \quad \frac{t_1/s \Downarrow Q_1 \quad (\forall (v', s') \in Q_1. ([v'/x] t_2)/s' \Downarrow Q)}{(\text{let } x = t_1 \text{ in } t_2)/s \Downarrow Q} \text{ OMNI-BIG-LET}$$

1929 One might wonder why we do not use OMNI-BIG-LET-CHAINED directly in the inductively defined rules. The reason is that Coq's *strict positivity* requirement on the well-formedness of inductive definitions does not allow it.

1930 To elaborate on this point, consider the four candidate Coq rules stated below.

1931 **Notation** "H1 H2" := ($\forall s, H1 s \rightarrow H2 s$). (* notation for entailment *)

1932 **Inductive** wp : trm \rightarrow (val \rightarrow state \rightarrow Prop) \rightarrow (state \rightarrow Prop) :=

1933 | wp_let_invalid : $\forall x t_1 t_2 Q$, (* non strictly positive occurrence of [wp]. *)
1934 wp t1 (fun v1 \Rightarrow wp (subst x v1 t2) Q)

1935 \vdash wp (trm_let x t1 t2) Q

1936 | wp_let_invalid' : $\forall Q_1 x t_1 t_2 Q s$, (* non strictly positive occurrence of [wp]. *)
1937 wp t1 Q1 s \rightarrow
1938 Q1 = (fun v1 s2 \Rightarrow wp (subst x v1 t2) Q s2) \rightarrow
1939 wp (trm_let x t1 t2) Q s

1940 | wp_let_valid : $\forall x t_1 t_2 Q$, (* accepted, but with useless induction principle *)
1941 (fun s \Rightarrow $\exists Q_1$, wp t1 Q1 s \wedge ($\forall v_1, Q_1 v_1 \vdash$ wp (subst x v1 t2) Q))
1942 \vdash wp (trm_let x t1 t2) Q

1943 | wp_let_valid' : $\forall x t_1 t_2 Q_1 Q$, (* accepted, with useful induction principle *)
1944 wp t1 Q1 s \rightarrow
1945 ($\forall v_1 s_2, Q_1 v_1 s_2 \rightarrow$ wp (subst x v1 t2) Q s2) \rightarrow
1946 wp (trm_let x t1 t2) Q s.

1947 The first rule directly translates WP-LET. It is rejected by Coq because it includes a non-strictly-positive occurrence of the predicate wp.

1948 The second rule attempts a reformulation by expanding the definition of entailment and by introducing a variable name Q_1 for the intermediate postcondition, together with an equality constraint on Q_1 . Yet, Coq rejects this rule just like the previous.

1949 The third rule modifies the first rule by introducing an existentially quantified intermediate postcondition Q_1 , quantifying over the items that belong to it. This rule is accepted by Coq. Yet, in that form, Coq (v8.14) generates a useless induction principle, which provides no induction

1961 hypothesis for the nested occurrence of wp. (This weakness can be corrected by stating and proving
1962 an induction principle manually, but we prefer to avoid the extra hassle.)

1963 The fourth rule corresponds to OMNI-BIG-LET. It adapts the previous rule by quantifying Q_1
1964 universally at the level of the constructor. This presentation is properly recognized by the induction-
1965 principle generator of Coq.

1966 B UNSPECIFIED EVALUATION ORDER

1967 For a language that uses unspecified but consistent order of evaluation for arguments of, e.g., pairs
1968 or applications, we can consider a generalized version of the rule OMNI-BIG-PAIR from the previous
1969 section. Essentially, we duplicate the premises to account for the two possible evaluation orders.
1970

$$\begin{array}{c}
 \text{OMNI-BIG-PAIR-UNSPECIFIED-ORDER} \\
 \frac{t_1/s \Downarrow Q_1 \quad (\forall (v_1, s') \in Q_1. t_2/s' \Downarrow \{(v_2, s'') \mid ((v_1, v_2), s'') \in Q\})}{t_2/s \Downarrow Q_2 \quad (\forall (v_2, s') \in Q_2. t_1/s' \Downarrow \{(v_1, s'') \mid ((v_1, v_2), s'') \in Q\})} \\
 (t_1, t_2)/s \Downarrow Q
 \end{array}$$

1971
1972
1973 To avoid the duplication in the premises, one can follow the approach described in §5.5 of the
1974 paper on the pretty-big-step semantics [Charguéraud 2013], which presents a general rule for
1975 evaluating a list of subterms in arbitrary order.
1976

1977 Note that we do not attempt to model languages that allow arbitrary interleavings in the eval-
1978 uation of arguments, as, e.g., arithmetic expressions in the C language [Krebbbers 2015]. More
1979 generally, concurrent evaluation is out of the scope of the present paper.
1980

1981 C OMNISEMANTICS RULES IN THE PRESENCE OF EXCEPTIONS

1982 For a programming language that features exceptions, the reasoning rule for let-bindings needs
1983 to be adapted in two ways. Indeed, if the body of the let-binding raises an exception, then the
1984 continuation should not be evaluated. Moreover, the exception raised should be included in the set
1985 of results that the let-binding can produce.
1986

1987 There are two ways to extend the grammar of results with exceptions. The first possibility is to
1988 add a constructor to the grammar of values. In this case, the postcondition Q remains a predicate
1989 over pairs of values and states. The second possibility is to introduce a type, to capture the sum
1990 of the type of values and of the type of exceptions. In that case, the postcondition Q becomes a
1991 predicate over pairs of results and states.
1992

1993 For simplicity, let us assume in what follows that the grammar of values includes a constant
1994 exception construct, written `exn`. In that setting, the omni-big-step evaluation rule for a let-binding
1995 of the form `(let $x = t_1$ in t_2)` can be stated as follows. The first premise describes the evaluation
1996 of t_1 . The second premise handles the case where t_1 produces a normal value. The third premise
1997 handles the case where t_1 produces an exception.
1998

$$\begin{array}{c}
 \text{OMNI-BIG-LET-WITH-EXCEPTIONS} \\
 \frac{t_1/s \Downarrow Q_1 \quad (\forall (v', s') \in Q_1. v' \neq \text{exn} \Rightarrow ([v'/x] t_2)/s' \Downarrow Q) \quad (\forall s'. Q_1 \text{ exn } s' \Rightarrow Q \text{ exn } s')}{(\text{let } x = t_1 \text{ in } t_2)/s \Downarrow Q}
 \end{array}$$

1999 We proved in Coq the equivalence of this treatment of exceptions with the formalization of
2000 exceptions expressed both in standard small-step and in standard big-step semantics.
2001

2002 D DEFINITION OF THE TERMINATION JUDGMENT

2003 We introduced the termination judgment to formalize the interpretation of the omni-big-step
2004 judgment (§2.2, OMNI-BIG-STEP-IFF-TERMINATES-AND-CORRECT). The predicate `terminates(t, s)`
2005

asserts that all executions of configuration t/s terminate. In this section, we present two formal definitions of this predicate, one in small-step style and one in big-step style.

The small-step version is inductively defined by the two rules show below.

$$\text{SMALL-TERMINATES-HERE} \quad \frac{\text{SMALL-TERMINATES-STEP} \quad \left(\exists t's'. t/s \longrightarrow t'/s' \right)}{\text{terminates}(v, s)} \quad \frac{\left(\forall t's'. (t/s \longrightarrow t'/s') \Rightarrow \text{terminates}(t', s') \right)}{\text{terminates}(t, s)}$$

The big-step version is inductively defined using one rule per language construct. We show below the rules for values and for let-bindings. This definition corresponds to an inductive version of the coinductive judgment safe from Wang et al. [2014], described in §7.

$$\text{BIG-TERMINATES-VAL} \quad \frac{\text{BIG-TERMINATES-LET} \quad \text{terminates}(t_1, s)}{\text{terminates}(v, s)} \quad \frac{\left(\forall v_1 s'. (t_1/s \Downarrow v_1/s') \Rightarrow \text{terminates}([v_1/x] t_2, s') \right)}{\text{terminates}(\text{let } x = t_1 \text{ in } t_2, s)}$$

E DEFINITION OF THE TYPING JUDGMENT

This section states the typing rules for the state-free language considered in §4.1. The typing rules are given for terms in A-normal form. The judgment $\vdash v : T$ asserts that the closed value v admits the type T . The judgment $E \vdash t : T$ asserts that the term t admits type T in the environment E . Finally, \mathbb{V} denotes the set of terms that are either values or variables.

$$\begin{array}{c} \text{VTYP-UNIT} \quad \text{VTYP-BOOL} \quad \text{VTYP-INT} \quad \text{VTYP-FIX} \\ \frac{}{\vdash \# : \text{unit}} \quad \frac{}{\vdash b : \text{bool}} \quad \frac{}{\vdash n : \text{int}} \quad \frac{f : (T_1 \rightarrow T_2), x : T_1 \vdash t : T_2}{\vdash ((\mu f. \lambda x. t)) : (T_1 \rightarrow T_2)} \\ \\ \text{TYP-VAL} \quad \text{TYP-VAR} \quad \text{TYP-FIX} \\ \frac{}{\vdash v : T} \quad \frac{x \in \text{dom } E \quad E[x] = T}{E \vdash x : T} \quad \frac{E, f : (T_1 \rightarrow T_2), x : T_1 \vdash t : T_2}{E \vdash (\mu f. \lambda x. t) : (T_1 \rightarrow T_2)} \\ \\ \text{TYP-APP} \\ \frac{E \vdash t_1 : (T_1 \rightarrow T_2) \quad E \vdash t_2 : T_1 \quad t_1, t_2 \in \mathbb{V}}{E \vdash (t_1 t_2) : T_2} \\ \\ \text{TYP-IF} \quad \text{TYP-LET} \\ \frac{E \vdash t_0 : \text{bool} \quad E \vdash t_1 : T \quad E \vdash t_2 : T \quad t_0 \in \mathbb{V}}{E \vdash (\text{if } t_0 \text{ then } t_1 \text{ else } t_2) : T} \quad \frac{E \vdash t_1 : T_1 \quad E, x : T_1 \vdash t_2 : T_2}{E \vdash (\text{let } x = t_1 \text{ in } t_2) : T_2} \\ \\ \text{TYP-ADD} \quad \text{TYP-RAND} \\ \frac{E \vdash t_1 : \text{int} \quad E \vdash t_2 : \text{int} \quad t_1, t_2 \in \mathbb{V}}{E \vdash (\text{add } t_1 t_2) : \text{int}} \quad \frac{E \vdash t_1 : \text{int} \quad t_1 \in \mathbb{V}}{E \vdash (\text{rand } t_1) : \text{int}} \end{array}$$

F EXTENSION OF THE TYPING JUDGMENT FOR STATE

This section states the typing rules for the imperative language considered in §4.2. There, the typing judgment for terms takes the form $S; E \vdash t : T$, and the typing judgment for closed values takes the form $S \vdash v : T$, where the store typing S maps locations to types. The rules from the previous appendix are extended simply to thread S throughout the judgment. The new rules include the rule

for typing locations and the rules for memory operations. They are shown next.

$$\begin{array}{c} \text{VTYP-LOC} \\ \frac{p \in \text{dom } S \quad S[p] = T}{S \vdash p : (\text{ref } T)} \end{array} \qquad \begin{array}{c} \text{TYP-REF} \\ \frac{S; E \vdash t_1 : T \quad t_1 \in \mathbb{V}}{S; E \vdash (\text{ref } t_1) : (\text{ref } T)} \end{array}$$

$$\begin{array}{c} \text{TYP-GET} \\ \frac{S; E \vdash t_1 : (\text{ref } T) \quad t_1 \in \mathbb{V}}{S; E \vdash (\text{get } t_1) : T} \end{array} \qquad \begin{array}{c} \text{TYP-SET} \\ \frac{S; E \vdash t_1 : (\text{ref } T) \quad S; E \vdash t_2 : T \quad t_1, t_2 \in \mathbb{V}}{S; E \vdash (\text{set } t_1 t_2) : \text{unit}} \end{array}$$

G DEFINITION OF THE STANDARD SMALL-STEP JUDGMENT

In §2.4, we gave a characterization of coinductive omni-big-step semantics in terms of the standard small-step semantics, written $t/s \longrightarrow t'/s'$. For reference, we give below the rules that define the standard small-step judgment:

$$\begin{array}{c} \text{SMALL-APP} \\ \frac{v_1 = (\mu f. \lambda x. t)}{(v_1 v_2)/s \longrightarrow ([v_2/x] [v_1/f] t)/s} \end{array} \qquad \begin{array}{c} \text{SMALL-IF-TRUE} \\ \frac{}{(\text{if true then } t_1 \text{ else } t_2)/s \longrightarrow t_1/s} \end{array}$$

$$\begin{array}{c} \text{SMALL-IF-FALSE} \\ \frac{}{(\text{if false then } t_1 \text{ else } t_2)/s \longrightarrow t_2/s} \end{array} \qquad \begin{array}{c} \text{SMALL-LET-CTX} \\ \frac{t_1/s \longrightarrow t'_1/s'}{(\text{let } x = t_1 \text{ in } t_2)/s \longrightarrow (\text{let } x = t'_1 \text{ in } t_2)/s'} \end{array}$$

$$\begin{array}{c} \text{SMALL-LET-VAL} \\ \frac{}{(\text{let } x = v_1 \text{ in } t_2)/s \longrightarrow ([v_1/x] t_2)/s} \end{array} \qquad \begin{array}{c} \text{SMALL-ADD} \\ \frac{}{(\text{add } n_1 n_2)/s \longrightarrow (n_1 + n_2)/s} \end{array} \qquad \begin{array}{c} \text{SMALL-RAND} \\ \frac{0 \leq m < n}{(\text{rand } n)/s \longrightarrow m/s} \end{array}$$

$$\begin{array}{c} \text{SMALL-REF} \\ \frac{p \notin \text{dom } s}{(\text{ref } v)/s \longrightarrow (s[p := v])/s} \end{array} \qquad \begin{array}{c} \text{SMALL-FREE} \\ \frac{p \in \text{dom } s}{(\text{free } p)/s \longrightarrow t/(s \setminus p)} \end{array}$$

$$\begin{array}{c} \text{SMALL-GET} \\ \frac{p \in \text{dom } s}{(\text{get } p)/s \longrightarrow (s[p])/s} \end{array} \qquad \begin{array}{c} \text{SMALL-SET} \\ \frac{p \in \text{dom } s}{(\text{set } p v)/s \longrightarrow t/(s[p := v])} \end{array}$$

H EVALUATION OF UNARY AND BINARY OPERATORS

The following definitions complete the semantics described in the case study “compiling immutable pairs to heap-allocated records” (§6.4).

$$\begin{array}{c} \frac{}{\text{evalunop}(\text{fst}, (v_1, v_2), v_1)} \qquad \frac{}{\text{evalunop}(\text{snd}, (v_1, v_2), v_2)} \qquad \frac{}{\text{evalunop}(\text{not}, 1, 0)} \\ \frac{}{\text{evalunop}(\text{not}, 0, 1)} \qquad \frac{}{\text{evalbinop}(+, n_1, n_2, n_1 + n_2)} \qquad \frac{}{\text{evalbinop}(\text{mkpair}, v_1, v_2, (v_1, v_2))} \end{array}$$